

VW Type

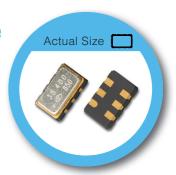
5.0 x 3.2mm SMD LVPECL/LVDS Voltage Controlled Crystal Oscillator

FEATURE

- Typical 5.0 x 3.2 x 1.25 mm 6 pads ceramic SMD package.
- Tight symmetry (45 to 55%) available.
- Tri-state enable/disable

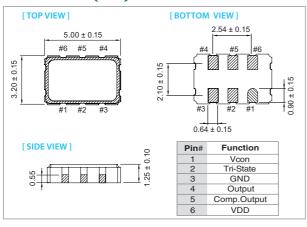
TYPICAL APPLICATION

- Set-top Box, HDTV
- WiMAX/WLAN
- xDSL/ VoIP, Cable modem

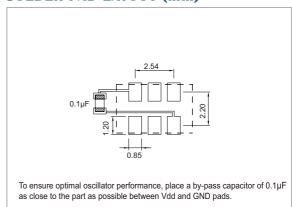


RoHS Compliant

DIMENSION (mm)



SOLDER PAD LAYOUT (mm)



ELECTRICAL SPECIFICATION

Parameter	LVPECL 3.3 V		LVDS 3.3 V		unit
	Supply Voltage Variation (VDD)	VDD-5%	VDD+5%	VDD-5%	VDD+5%
Frequency Range	30	250	30	250	MH
Standard Frequency	122.88, 153.6, 155.52, 156.25			IVITIZ	
Absolute Pulling Range (APR)	±50	_	±50	_	ppm
Control Voltage Range	0.3	3.0	0.3	3.0	V
Supply Current 30 MHz ≤ Fo ≤ 250 MHz		100		75	mA
Output Level					
Output High (Logic "1")	2.275	_	_	1.6	V
Output Low (Logic "0")	_	1.68	0.9	_	
Transition Time: Rise/Fall Time+	-	1.0	_	1.0	nSe
Start Time	_	3	_	3	mSe
Tri-State (input to Pin 2)					
Enable (High voltage or floating)	2.31	_	2.31	_	V
Disable (Low voltage or GND)	_	0.99	_	0.99	•
Linearity	_	10	_	10	%
Modulation Bandwidth (BW)	20	_	20	_	kHz
Input Impedance	10000	_	10000	_	ΚΩ
RMS Phase Jitter					
Fo < 100 MHz	_	1.0	_	1.0	pSec
100 MHz ≦ Fo < 125 MHz		0.7	_	0.7	
125 MHz ≦ Fo < 150 MHz		0.5	_	0.5	
150 MHz ≦ Fo ≦ 250 MHz	_	0.3	_	0.3	
Phase Noise@122.88MHz 100 Hz	-105		-105		
1 KHz	-128		-128		dBc/l
10 KHz	-145		-145		
Aging (@ 25°C 1st year)	_	±3	-	±3	ppm
Storage Temp. Range	-55	125	-55	125	°C

Standard frequencies are frequencies which the crystal has been designed and does not imply a stock position.

FREQ. STABILITY vs. TEMP. RANGE

Temp. (°C)	±25	±50
-10 ~ +60	0	0
-20 ~ +70	0	0
-40 ~ +85	×	0

- $^*\bigcirc$: Available \triangle :Conditional X: Not available
- * Inclusive of calibration @ 25 °C, operating temperature range, input voltage variation, load variation, aging (1st year), shock, and vibration

Note: not all combination of options are available. Other specifications may be available upon request.

⁺ Transition times are measured between 20% and 80% of VDD.