AUTOMOTIVE

RoHS

COMPLIANT

HALOGEN

FREE



Vishay General Semiconductor

Surface Mount Trench MOS Barrier Schottky Rectifier

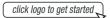
TMBS® eSMP® Series



MicroSMP (DO-219AD)



DESIGN SUPPORT TOOLS





PRIMARY CHARACTERISTICS			
I _{F(AV)}	1.0 A		
V _{RRM}	60 V		
I _{FSM}	25 A		
V _F at I _F = 1.0 A (125 °C)	0.45 V		
T _J max.	150 °C		
Package	MicroSMP (DO-219AD)		
Circuit configuration	Single		

FEATURES





- Trench MOS Schottky technology
- Low forward voltage drop
- Low power loss, high efficiency
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available
 - Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

For use in low voltage high frequency inverters, freewheeling, DC/DC converters, and polarity protection applications, in commercial, industrial, and automotive applications.

MECHANICAL DATA

Case: MicroSMP (DO-219AD)

Molding compound meets UL 94 V-0 flammability rating

Base P/N-M3 - halogen-free, and RoHS-compliant

Base P/NHM3 - halogen-free, RoHS-compliant, and

AEC-Q101 qualified

Terminals: matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 and HM3 suffix meets JESD 201 class 2 whisker test

Polarity: color band denotes the cathode end

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	V1P6	UNIT	
Device marking code		V16		
Maximum repetitive peak reverse voltage	V_{RRM}	60	V	
Maximum DC forward current	I _{F(AV)}	1.0		
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	I _{FSM}	25	A	
Operating junction and storage temperature range	T _J ⁽¹⁾ , T _{STG}	-40 to +150	°C	

Note

(1) The heat generated must be less than the thermal conductivity from junction to ambient: $dP_D/dT_J < 1/R_{B,IA}$



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ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)						
PARAMETER	TEST C	ONDITIONS	SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage per diode	I _F = 0.5 A	T _A = 25 °C T _A = 125 °C	V _F ⁽¹⁾	0.45	-	V
	I _F = 1.0 A			0.52	0.60	
	I _F = 0.5 A			0.35	-	
	I _F = 1.0 A			0.45	0.53	
Reverse current per diode	V _R = 60 V	T _A = 25 °C	I _R ⁽²⁾	-	0.15	- mA
		T _A = 125 °C		1	5	
Typical junction capacitance	4.0 V, 1 MHz		CJ	130	-	pF

Notes

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: pulse width ≤ 5 ms

THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)			
PARAMETER	SYMBOL	SYMBOL V1PM10	
Typical thermal resistance	R _{0JA} ⁽¹⁾⁽²⁾ 130		°C/W
Typical trieffial resistance	R _{0JM} (3)	20	C/VV

Notes

- $^{(1)}$ The heat generated must be less than the thermal conductivity from junction-to-ambient: $dP_D/dT_J < 1/R_{\theta JA}$
- (2) Free air, mounted on FR4 PCB, 2 oz. standard footprint, $R_{\theta JA}$ junction to ambient
- Mounted on FR4 PCB, 2 oz. standard footprint, $R_{\theta JM}$ junction to mount

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
V1P6-M3/H	0.006	Н	4500	7" diameter plastic tape and reel	
V1P6HM3/H (1)	0.006	Н	4500	7" diameter plastic tape and reel	

Note

(1) AEC-Q101 qualified

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

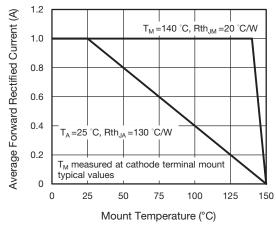


Fig. 1 - Maximum Forward Current Derating Curve

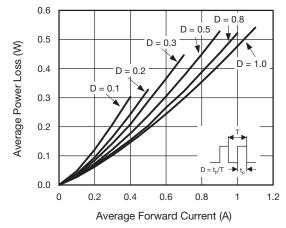


Fig. 2 - Average Power Loss Characteristics



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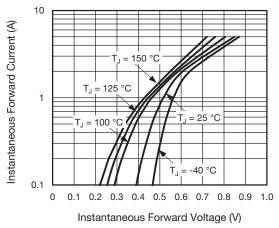


Fig. 3 - Typical Instantaneous Forward Characteristics

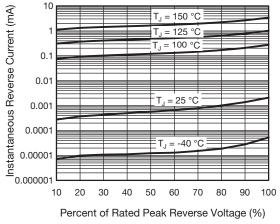


Fig. 4 - Typical Reverse Leakage Characteristics

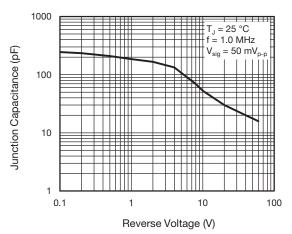


Fig. 5 - Typical Junction Capacitance

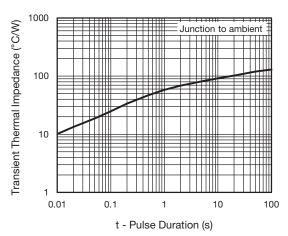
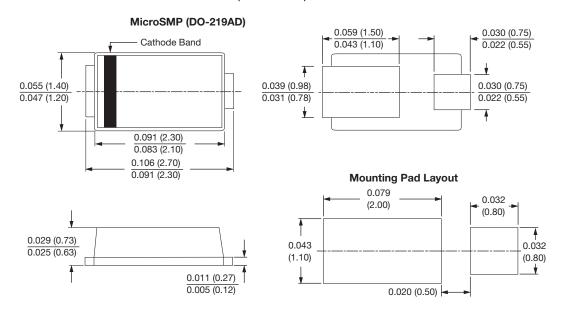


Fig. 6 - Typical Transient Thermal Impedance

PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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