Vishay General Semiconductor

Dual High-Voltage Trench MOS Barrier Schottky Rectifier

Ultra Low $V_F = 0.59$ V at $I_F = 5.0$ A



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DESIGN SUPPORT TOOLS



PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 x 10 A			
V _{RRM}	150 V			
I _{FSM}	120 A			
V_F at I_F = 10 A	0.69 V			
T _J max.	150 °C			
Package	D ² PAK (TO-263AB)			
Circuit configuration Common cathode				

FEATURES

- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

For use in high frequency converters, switching power supplies, freewheeling diodes, OR-ing diode, DC/DC converters, and reverse battery protection.

MECHANICAL DATA

Case: D²PAK (TO-263AB)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: as marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER		SYMBOL	VB20150C	UNIT		
Maximum repetitive peak reverse voltage		V _{RRM}	150	V		
Maximum average forward rectified current (fig. 1)	per device	levus.	20	٨		
	per diode	IF(AV)	10	— A		
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load		I _{FSM}	120	А		
Voltage rate of change (rated V _R)		dV/dt	10 000	V/µs		
Operating junction and storage temperature range		T _J , T _{STG}	-55 to +150	°C		

ELECTRICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode ⁽¹⁾	$I_{F} = 5.0 \text{ A}$	T _A = 25 °C	V _F	0.79	-	V	
	I _F = 10 A			1.05	1.20		
	I _F = 5.0 A	T _A = 125 °C		0.59	-		
	I _F = 10 A			0.69	0.75		
Reverse current per diode ⁽²⁾	V _R = 100 V	T _A = 25 °C	I _R	1.3	-	μA	
	$v_{\rm R} = 100 v$	T _A = 125 °C		1.2	-	mA	
	V _R = 150 V	T _A = 25 °C		-	150	μA	
	$v_{\rm R} = 150$ V	T _A = 125 °C		3	15	mA	

Notes

 $^{(1)}\,$ Pulse test: 300 μs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: Pulse width \leq 40 ms

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 1
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THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL	VB20150C	UNIT	
Typical thermal resistance per diode	$R_{\theta JC}$	2.8	°C/W	

ORDERING INFORMATION (Example)							
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
TO-263AB	VB20150C-M3/4W	1.39	4W	50/tube	Tube		
TO-263AB	VB20150C-M3/8W	1.39	8W	800/reel	Tape and reel		

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

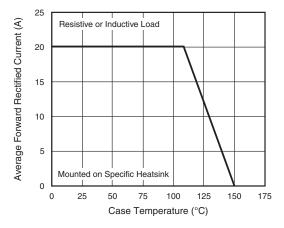


Fig. 1 - Maximum Forward Current Derating Curve

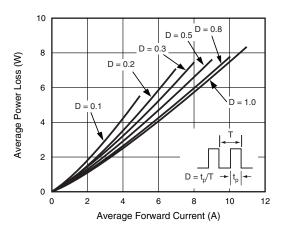


Fig. 2 - Forward Power Dissipation Characteristics Per Diode

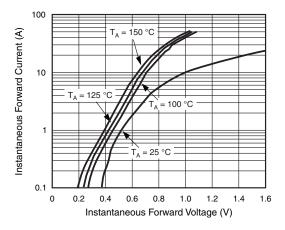


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

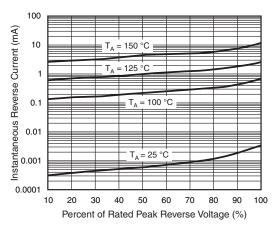
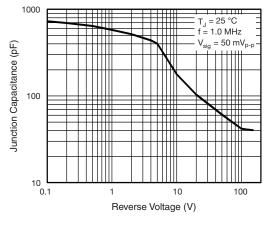


Fig. 4 - Typical Reverse Characteristics Per Diode



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Fig. 5 - Typical Junction Capacitance Per Diode

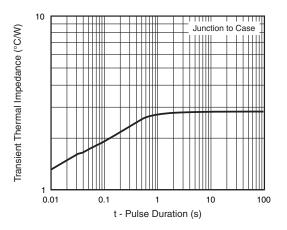
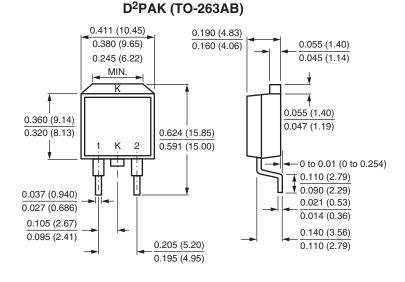
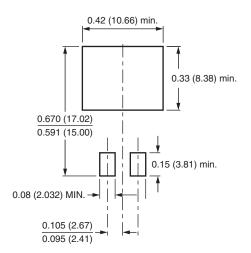


Fig. 6 - Typical Transient Thermal Impedance Per Diode

PACKAGE OUTLINE DIMENSIONS in inches (millimeters)



Mounting Pad Layout





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