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April 2015

FGH60N60SFDTU_F085 600 V, 60 A Field Stop IGBT

Features

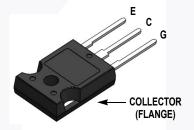
- · High Current Capability
- Low Saturation Voltage: V_{CE(sat)} = 2.2 V @ I_C = 60 A
- · High Input Impedance
- · Fast Switching
- · RoHS Compliant
- · Qualified to Automotive Requirements of AEC-Q101

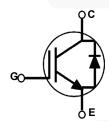
Applications

- · Automotive chargers, Converters, High Voltage Auxiliaries
- · Inverters, PFC, UPS

General Description

Using Novel Field Stop IGBT Technology, Fairchild's new series of Field Stop IGBTs offer the optimum performance for Automotive Chargers, Inverter, and other applications where low conduction and switching losses are essential.





Absolute Maximum Ratings

Symbol	Description		Ratings	Unit	
V _{CES}	Collector to Emitter Voltage		600	V	
V	Gate to Emitter Voltage		±20	V	
V_{GES}	Transient Gate-to-Emitter Voltage		±30	V	
I _C	Collector Current	@ T _C = 25°C	120	Α	
10	Collector Current	@ T _C = 100°C	60	А	
I _{CM (1)}	Pulsed Collector Current	@ T _C = 25°C	180	A	
P_{D}	Maximum Power Dissipation	@ T _C = 25°C	378	W	
' D	Maximum Power Dissipation	@ T _C = 100°C	151	W	
TJ	Operating Junction Temperature		-55 to +150	°C	
T _{stg}	Storage Temperature Range		-55 to +150	°C	
T _L	Maximum Lead Temp. for soldering Purposes, 1/8" from case for 5 seconds	300	°C		

Notes:

1: Repetitive test, Pulse width limited by max. juntion temperature

Thermal Characteristics

Symbol	Parameter	Тур.	Unit
$R_{\theta JC}(IGBT)$	Thermal Resistance, Junction to Case	0.33	°C/W
$R_{\theta JC}(Diode)$	Thermal Resistance, Junction to Case	1.1	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	40	°C/W

Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FGH60N60SFDTU_F085	FGH60N60SFD	TO-247	Tube	N/A	N/A	30

Electrical Characteristics of the IGBT $T_C = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Off Charac	eteristics					
BV _{CES}	Collector to Emitter Breakdown Voltage	$V_{GE} = 0 \text{ V}, I_{C} = 250 \mu\text{A}$	600	-	-	V
ΔBV _{CES}	Temperature Coefficient of Breakdown Voltage	$V_{GE} = 0 \text{ V, } I_{C} = 250 \mu\text{A}$	-	0.4	-	V/°C
I _{CES}	Collector Cut-Off Current	V _{CE} = V _{CES} , V _{GE} = 0 V	-	-	250	μА
I _{GES}	G-E Leakage Current	$V_{GE} = V_{GES}, V_{CE} = 0 V$	-	-	±400	nA
On Charac	teristics					
V _{GE(th)}	G-E Threshold Voltage	$I_C = 250 \mu A, V_{CE} = V_{GE}$	4.0	5.1	6.6	V
1		I _C = 60 A, V _{GE} = 15 V	-	2.2	2.9	V
V _{CE(sat)}	Collector to Emitter Saturation Voltage	I _C = 60 A, V _{GE} = 15 V, T _C = 125°C	-\	2.4	-	V
Dynamic C	Characteristics					
C _{ies}	Input Capacitance		-	2940	-	pF
C _{oes}	Output Capacitance	$V_{CE} = 30 \text{ V}, V_{GE} = 0 \text{ V},$ f = 1 MHz	-	310	-	pF
C _{res}	Reverse Transfer Capacitance	- 1 - 1 IVITIZ	-	100	-	pF
Switching	Characteristics					
t _{d(on)}	Turn-On Delay Time		-	26	-	ns
t _r	Rise Time	V_{CC} = 400 V, I_{C} = 60 A, R_{G} = 5 Ω , V_{GE} = 15 V, Inductive Load, T_{C} = 25°C	-	54	-	ns
t _{d(off)}	Turn-Off Delay Time		-	134	-	ns
t _f	Fall Time		-	18	62	ns
E _{on}	Turn-On Switching Loss		-	1.97	-	mJ
E _{off}	Turn-Off Switching Loss		- //	0.57	-	mJ
E _{ts}	Total Switching Loss		-//	2.54	-	mJ
t _{d(on)}	Turn-On Delay Time		-	26	- /	ns
t _r	Rise Time		-	50	- 7	ns
t _{d(off)}	Turn-Off Delay Time	$V_{CC} = 400 \text{ V}, I_{C} = 60 \text{ A},$	-	142	-	ns
t _f	Fall Time	$R_G = 5 \Omega$, $V_{GE} = 15 V$,	-	24	// -	ns
E _{on}	Turn-On Switching Loss	Inductive Load, T _C = 125°C	-	2.5	- //	mJ
E _{off}	Turn-Off Switching Loss		-	0.8	- (mJ
E _{ts}	Total Switching Loss		-	3.2	-	mJ
Qg	Total Gate Charge		-	188	-	nC
Q _{ge}	Gate to Emitter Charge	$V_{CE} = 400 \text{ V}, I_{C} = 60 \text{ A},$ $V_{GE} = 15 \text{ V}$	-	21	-	nC
Q _{gc}	Gate to Collector Charge	▼GE 10 ▼	-	98	-	nC

Electrical Characteristics of the Diode $T_C = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Test Conditions		Min.	Тур.	Max	Unit
V _{FM}	Diode Forward Voltage	I _E = 30 A	$T_{\rm C} = 25^{\rm o}{\rm C}$	-	1.9	2.6	V
FINI	2.000 : 0.110.00	.,	$T_{\rm C}$ = 125 $^{\rm o}$ C	-	1.7	-	1
t _{rr}	Diode Reverse Recovery Time	I _F = 30 A, di _F /dt = 200 A/μs	$T_{\rm C} = 25^{\rm o}{\rm C}$	-	55	-	ns
11			$T_{\rm C}$ = 125°C	-	204	-	
Q _{rr}	Diode Reverse Recovery Charge		$T_C = 25^{\circ}C$	-	125	1	nC
			$T_{\rm C}$ = 125°C	-	895	ı	

Figure 1. Typical Output Characteristics

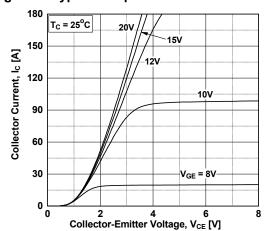


Figure 3. Typical Saturation Voltage Characteristics

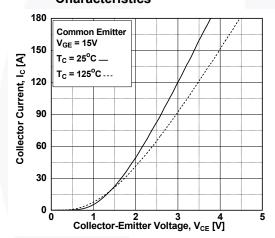


Figure 5. Saturation Voltage vs. Case
Temperature at Variant Current Level

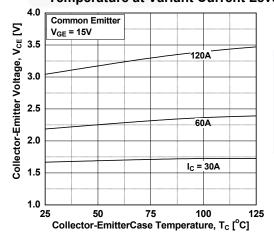


Figure 2. Typical Output Characteristics

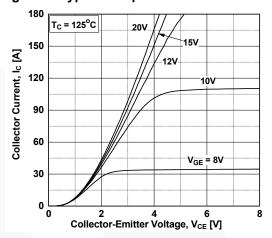


Figure 4. Transfer Characteristics

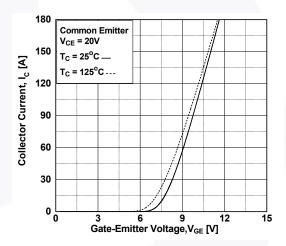


Figure 6. Saturation Voltage vs. V_{GE}

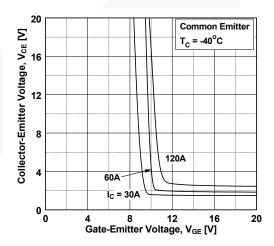


Figure 7. Saturation Voltage vs. V_{GE}

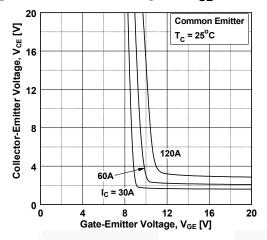


Figure 9. Capacitance Characteristics

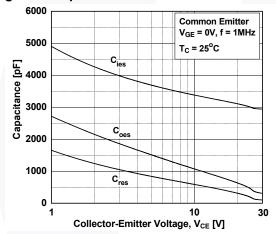


Figure 11. SOA Characteristics

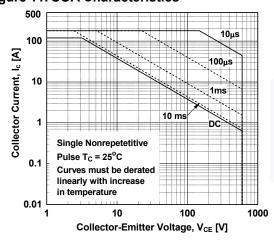


Figure 8. Saturation Voltage vs. V_{GE}

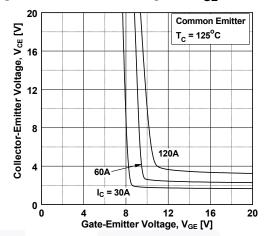


Figure 10. Gate charge Characteristics

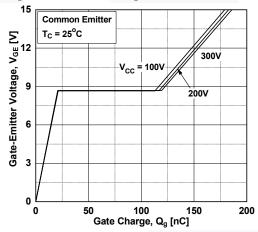


Figure 12. Turn off Switching SOA Characteristics

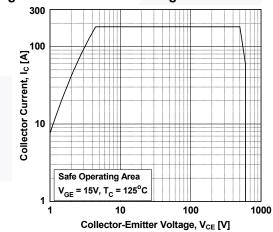


Figure 13. Turn-on Characteristics vs.
Gate Resistance

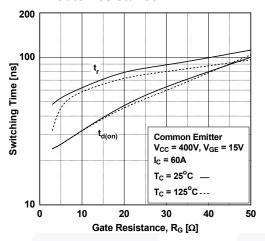


Figure 15. Turn-on Characteristics vs. Collector Current

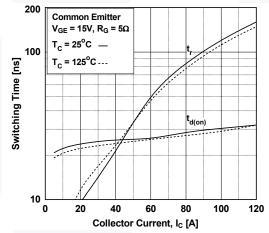


Figure 17. Switching Loss vs Gate Resistance

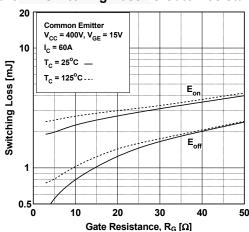


Figure 14. Turn-off Characteristics vs.
Gate Resistance

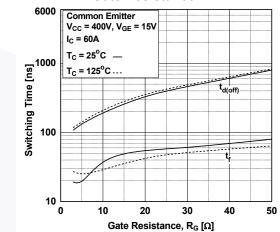


Figure 16. Turn-off Characteristics vs.
Collector Current

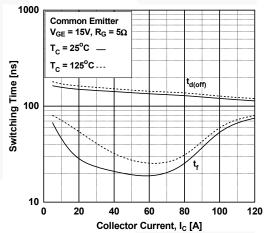


Figure 18. Switching Loss vs Collector Current

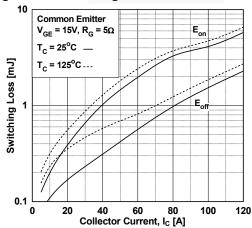


Figure 19. Forward Characteristics

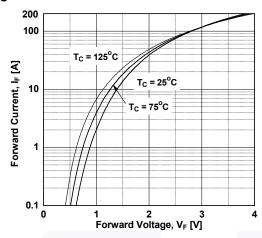


Figure 20. Reverse Current

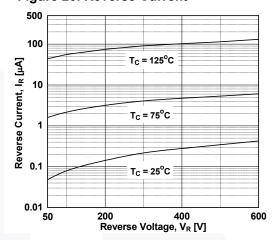


Figure 21. Stored Charge

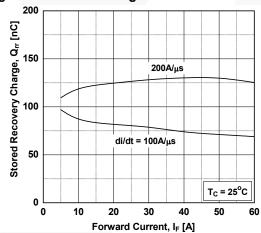


Figure 22. Reverse Recovery Time

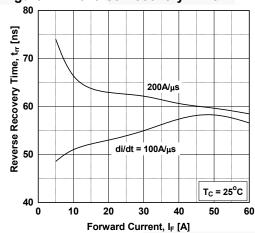
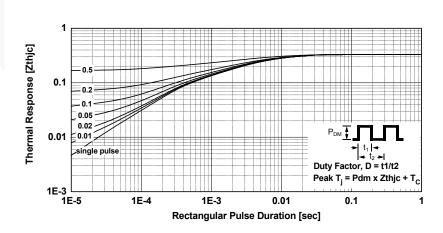
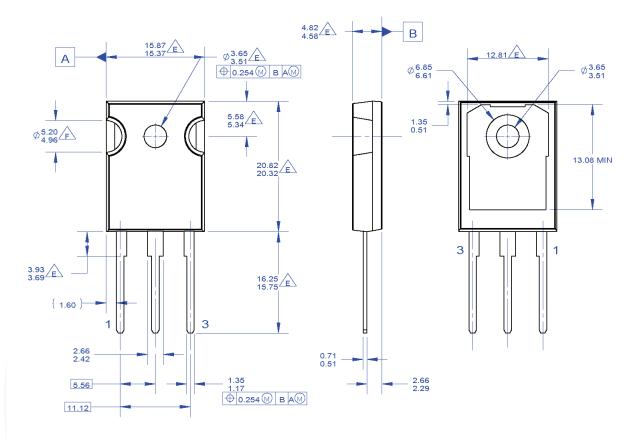


Figure 23. Transient Thermal Impedance of IGBT



Mechanical Dimensions



NOTES: UNLESS OTHERWISE SPECIFIED.

- A. PACKAGE REFERENCE: JEDEC TO-247, ISSUE E, VARIATION AB, DATED JUNE, 2004.
- B. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH. AND TIE BAR EXTRUSIONS.
- C. ALL DIMENSIONS ARE IN MILLIMETERS.
- D. DRAWING CONFORMS TO ASME Y14.5 1994
- DOES NOT COMPLY JEDEC STANDARD VALUE
- NOTCH MAY BE SQUARE
- G. DRAWING FILENAME: MKT-TO247A03_REV03

Figure 24. TO-247 3L - TO-247, MOLDED, 3 LEAD, JEDEC VARIATION AB

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		Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary First Production		Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
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