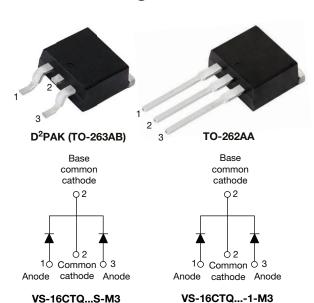


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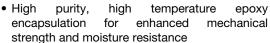
# High Performance Schottky Rectifier, 2 x 8 A



PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	2 x 8 A				
$V_{R}$	60 V, 80 V, 100 V				
V <sub>F</sub> at I <sub>F</sub>	0.58 V				
I <sub>RM</sub>	7.0 mA at 125 °C				
T <sub>J</sub> max.	175 °C				
E <sub>AS</sub>	7.5 mJ				
Package	D <sup>2</sup> PAK (TO-263AB), TO-262AA				
Circuit configuration	Common cathode				

#### **FEATURES**

- 175 °C T<sub>J</sub> operation
- Center tap configuration
- Low forward voltage drop





COMPLIANT

- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

#### **DESCRIPTION**

This center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I <sub>F(AV)</sub>	Rectangular waveform	16	A		
V <sub>RRM</sub>		60 to 100	V		
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	850	A		
V <sub>F</sub>	8 A <sub>pk</sub> , T <sub>J</sub> = 125 °C (per leg)	0.58	V		
TJ	Range	-55 to +175	°C		

VOLTAGE RATINGS						
PARAMETER	SYMBOL	VS-16CTQ060S-M3 VS-16CTQ060-1-M3	VS-16CTQ080S-M3 VS-16CTQ080-1-M3	VS-16CTQ100S-M3 VS-16CTQ100-1-M3	UNITS	
Maximum DC reverse voltage	$V_{R}$	60	80	100	V	
Maximum working peak reverse voltage	$V_{RWM}$	00	60	100	V	



# VS-16CTQ...S-M3, VS-16CTQ...-1-M3 Series

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ABSOLUTE MAXIMU	ABSOLUTE MAXIMUM RATINGS								
PARAMETER	PARAMETER		TEST COND	TEST CONDITIONS		UNITS			
Maximum average	ximum average per leg		8						
forward current See fig. 5	per device	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>C</sub> = 148 °C, rectangular waveform		16	Α			
Maximum peak one cycle			5 μs sine or 3 μs rect. pulse	Following any rated load	850				
non-repetitive surge current per leg See fig. 7		I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	condition and with rated V <sub>RRM</sub> applied	275	Α			
Non-repetitive avalanche energy per leg		E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 0.50 A, L = 60 mH		7.50	mJ			
Repetitive avalanche current per leg		I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		0.50	Α			

ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS		
		8 A	T <sub>.1</sub> = 25 °C	0.72	V		
Maximum forward voltage drop per leg See fig. 1	V <sub>FM</sub> <sup>(1)</sup>	16 A	1j=25 C	0.88			
	VFM ('')	8 A	T <sub>.1</sub> = 125 °C	0.58			
		16 A	1 <sub>J</sub> = 125 C	0.69			
Maximum reverse leakage current per leg	ı (1)	T <sub>J</sub> = 25 °C	V Detect V	0.55	mA		
See fig. 2	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 125 °C	V <sub>R</sub> = Rated V <sub>R</sub>	7.0			
Threshold voltage	V <sub>F(TO)</sub>	T T mayimum		0.415	V		
Forward slope resistance	r <sub>t</sub>	I j = I j maximum		11.07	mΩ		
Maximum junction capacitance per leg	C <sub>T</sub>	V <sub>R</sub> = 5 V <sub>DC</sub> (test signal range	500	pF			
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 mm from package body 8.0			nΗ		
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub> 10 000 V/					

#### Note

 $<sup>^{(1)}\,</sup>$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>	J, T <sub>Stg</sub>		°C	
Maximum thermal resistance, junction to case per leg		٥	B. Bounding			
Maximum thermal resistance, junction to case per package		R <sub>thJC</sub>	DC operation	1.63	°C/W	
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased	0.50		
Approximate weight				2	g	
Approximate weight				0.07	OZ.	
Manusting torque	minimum			6 (5)	kgf · cm	
Mounting torque	maximum			12 (10)	(lbf · in)	
Madda da ta			Case style D <sup>2</sup> PAK (TO-263AB)	16CT	QS	
Marking device			Case style TO-262AA	16CT	Q1	



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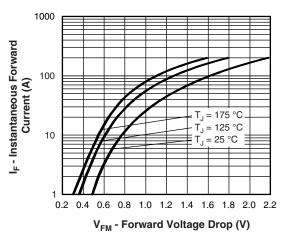


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

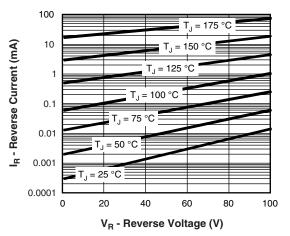


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

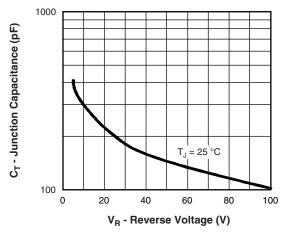


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

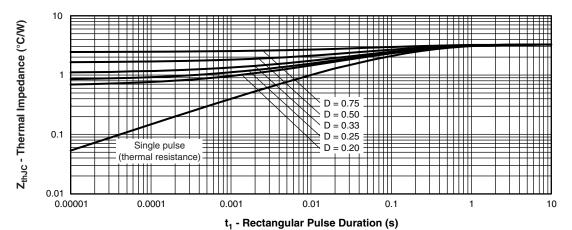


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics (Per Leg)

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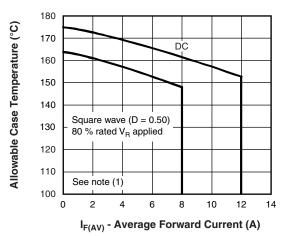


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

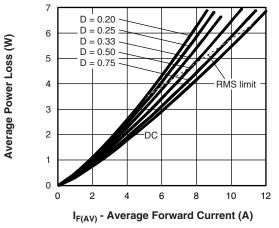


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

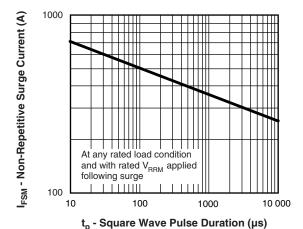


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

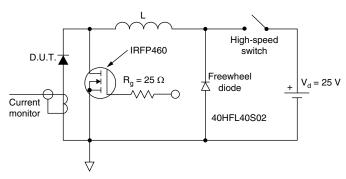


Fig. 8 - Unclamped Inductive Test Circuit

#### Note

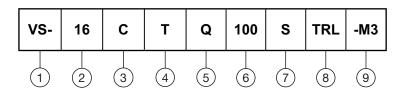
Formula used: T<sub>C</sub> = T<sub>J</sub> - (Pd + Pd<sub>REV</sub>) x R<sub>th,JC</sub>; Pd = forward power loss = I<sub>F(AV)</sub> x V<sub>FM</sub> at (I<sub>F(AV)</sub>/D) (see fig. 6); Pd<sub>REV</sub> = inverse power loss = V<sub>R1</sub> x I<sub>R</sub> (1 - D); I<sub>R</sub> at V<sub>R1</sub> = 80 % rated V<sub>R</sub> applied



## Vishay Semiconductors

#### **ORDERING INFORMATION TABLE**

**Device code** 



1 - Vishay Semiconductors product

2 - Current rating (16 A)

Circuit configuration: C = common cathode

**4** - T = TO-220

5 - Schottky "Q" series 060 = 60 V 6 - Voltage ratings 080 = 80 V 100 = 100 V

7 - • S =  $D^2PAK$  (TO-263AB)

• -1 = TO-262AA

**8** - • None = tube

• TRL = tape and reel (left oriented - for D<sup>2</sup>PAK (TO-263AB) only)

• TRR = tape and reel (right oriented - for D<sup>2</sup>PAK (TO-263AB) only)

9 - -M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free

ORDERING INFORMATION						
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-16CTQ060S-M3	50	1000	Antistatic plastic tubes			
VS-16CTQ060STRR-M3	800	800	13" diameter reel			
VS-16CTQ060STRL-M3	800	800	13" diameter reel			
VS-16CTQ060-1-M3	50	1000	Antistatic plastic tubes			
VS-16CTQ080S-M3	50	1000	Antistatic plastic tubes			
VS-16CTQ080STRR-M3	800	800	13" diameter reel			
VS-16CTQ080STRL-M3	800	800	13" diameter reel			
VS-16CTQ080-1-M3	50	1000	Antistatic plastic tubes			
VS-16CTQ100S-M3	50	1000	Antistatic plastic tubes			
VS-16CTQ100STRR-M3	800	800	13" diameter reel			
VS-16CTQ100STRL-M3	800	800	13" diameter reel			
VS-16CTQ100-1-M3	50	1000	Antistatic plastic tubes			

LINKS TO RELATED DOCUMENTS					
Dimensions	D <sup>2</sup> PAK (TO-263AB)	www.vishay.com/doc?96164			
Dimensions	TO-262AA	www.vishay.com/doc?96165			
Dark manding information	D <sup>2</sup> PAK (TO-263AB)	www.vishay.com/doc?95444			
Part marking information	TO-262AA	www.vishay.com/doc?95443			
Packaging information		www.vishay.com/doc?96424			
SPICE model		www.vishay.com/doc?95279			



## Vishay Semiconductors

## D<sup>2</sup>PAK

#### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIMETERS		INCHES		NOTES	SYMBOL	MILLIM	ETERS	INC	HES	NOTES
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOIES	STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.06	4.83	0.160	0.190		D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010		Е	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039		E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4	е	2.54	BSC	0.100	) BSC	
b2	1.14	1.78	0.045	0.070		Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4	L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029		L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4	L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065		L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2	L4	4.78	5.28	0.188	0.208	

#### Notes

- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inch
- (7) Outline conforms to JEDEC® outline TO-263AB

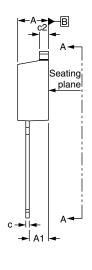


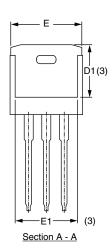
## Vishay Semiconductors

## **TO-262**

#### **DIMENSIONS** in millimeters and inches

# Modified JEDEC outline TO-262 (Datum A) (2) (3) (3) L1 D D D C C C C A (2) A (2) A (3) L2 B B B B C C C A (2)



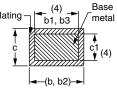


**⊕** 0.010**⋒**|A**⋒**|B

#### Lead assignments



<u>Diodes</u>
1. - Anode (two die)/open (one die)
2., 4. - Cathode
3. - Anode



Section B - B and C - C Scale: None

SYMBOL	MILLIN	METERS	INCH	INCHES			
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES		
Α	4.06	4.83	0.160	0.190			
A1	2.03	3.02	0.080	0.119			
b	0.51	0.99	0.020	0.039			
b1	0.51	0.89	0.020	0.035	4		
b2	1.14	1.78	0.045	0.070			
b3	1.14	1.73	0.045	0.068	4		
С	0.38	0.74	0.015	0.029			
c1	0.38	0.58	0.015	0.023	4		
c2	1.14	1.65	0.045	0.065			
D	8.51	9.65	0.335	0.380	2		
D1	6.86	8.00	0.270	0.315	3		
E	9.65	10.67	0.380	0.420	2, 3		
E1	7.90	8.80	0.311	0.346	3		
е	2.54	BSC	0.100	BSC			
L	13.46	14.10	0.530	0.555			
L1	-	1.65	-	0.065	3		
L2	3.56	3.71	0.140	0.146			

#### **Notes**

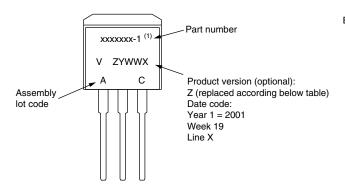
- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- $^{(3)}$  Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Controlling dimension: inches
- (6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum) and D1 (minimum) where dimensions derived the actual package outline



## **Part Marking Information**

## Vishay Semiconductors

## **TO-262**



Example: This is a xxxxxxx-1 <sup>(1)</sup> with assembly lot code AC, assembled on WW 19, 2001

in the assembly line "X"

#### Note

(1) If part number contain "H" as last digit, product is AEC-Q101 qualified

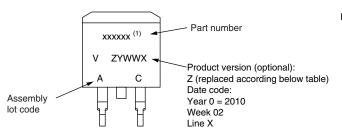
ENVIRONMENTAL NAMING CODE (Z)	PRODUCT DEFINITION			
A	Termination lead (Pb)-free			
В	Totally lead (Pb)-free			
E	RoHS-compliant and termination lead (Pb)-free			
F	RoHS-compliant and totally lead (Pb)-free			
М	Halogen-free, RoHS-compliant and termination lead (Pb)-free			
N	Halogen-free, RoHS-compliant and totally lead (Pb)-free			
G	Green			



# **Part Marking Information**

Vishay Semiconductors

## D<sup>2</sup>PAK



Example: This is a xxxxxx <sup>(1)</sup> with assembly lot code AC, assembled on WW 02, 2010

#### Note

(1) If part number contain "H" as last digit, product is AEC-Q101 qualified

ENVIRONMENTAL NAMING CODE (Z)	PRODUCT DEFINITION		
А	Termination lead (Pb)-free		
B Totally lead (Pb)-free			
E	RoHS-compliant and termination lead (Pb)-free		
F	RoHS-compliant and totally lead (Pb)-free		
M	Halogen-free, RoHS-compliant, and termination lead (Pb)-free		
N	Halogen-free, RoHS-compliant, and totally lead (Pb)-free		
G	Green		



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