# Application

- · Motor drive
- · Inverter, Converter
- · Photovoltaics, wind power generation.
- · Induction heating equipment.

### Features

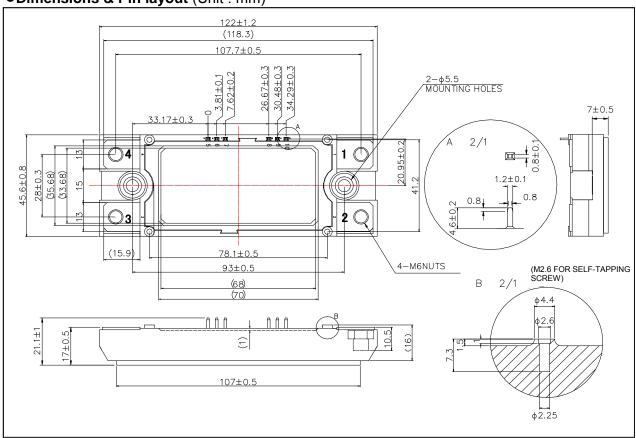
- 1) Low surge, low switching loss.
- 2) High-speed switching possible.
- 3) Reduced temperature dependence.

# \*Do not connect to NC pin.

### Construction

This product is a half bridge module consisting of SiC-DMOS from ROHM.

# ●Dimensions & Pin layout (Unit : mm)

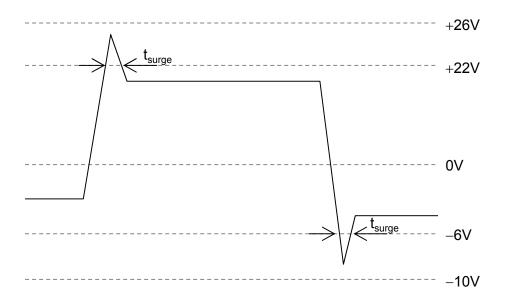


# ● Absolute maximum ratings (Tj = 25°C)

Parameter	Symbol	Conditions	Limit	Unit
Drain-source voltage	$V_{DSS}$	G-S short	1200	V
Gate-source voltage(+)	W	D-S short	22	V
Gate-source voltage(-)	$V_{GSS}$	D-S short	<b>–6</b>	V
G - S Voltage (tsurge<300nsec V <sub>GS</sub>		D-S short	-10 to +26	°C
D.::	I <sub>D</sub>	DC(Tc=60°C)	204	Α
Drain current *1	I <sub>DRM</sub>	Pulse (Tc=60°C) 1ms *2	360	Α
Source current *1	I <sub>S</sub>	Tc=60°C V <sub>GS</sub> =18V	204	Α
	I <sub>SRM</sub>	Pulse (Tc=60°C) 1ms V <sub>GS</sub> =18V * <sup>2</sup>	360	Α
		Pulse (Tc=60°C) 10μs V <sub>GS</sub> =0V * <sup>2</sup>	1360	Α
Total power disspation *4	Ptot	Tc=25°C	175	W
Max Junction Temperature	Tjmax		-40 to150	°C
Storage temperature	Tstg		-40 to125	°C
Isolation voltage	Visol	Terminals to baseplate, f=60Hz AC 1min.	2500	Vrms
Mounting torque	-	Main Terminals : M6 screw	4.5	N·m
Mounting torque		Mounting to heat shink: M5 screw	3.5	N·m

<sup>(\*1)</sup> Case temperature (T<sub>c</sub>) is defined on the surface of base plate just under the chips.

# Example of acceptable VGS waveform



<sup>(\*2)</sup> Repetition rate should be kept within the range where temperature rise if die should not exceed T<sub>jmax</sub>.

<sup>(\*3)</sup> T<sub>i</sub> is less than 175°C

### ●Electrical characteristics (Tj=25°C)

Parameter	Symbol	Conditions		Min.	Тур.	Max.	Unit
Static drain-source on-state voltage	V <sub>DS(on)</sub>	I <sub>C</sub> =180A, V <sub>GS</sub> =18V	Tj=25°C	-	2.3	3.2	V
			Tj=125°C	-	3.3	4.4	
			Tj=150°C	-	3.6	5	
Drain cutoff current	I <sub>DSS</sub>	V <sub>DS</sub> =1200V, V <sub>GS</sub> =0V		-	-	10	μΑ
Source-drain voltage	$V_{SD}$	V <sub>GS</sub> =0V, I <sub>S</sub> =180A	Tj=25°C	1	5.4	-	V
			Tj=125°C	1	5.1	-	
			Tj=150°C	ı	4.8	-	
		V <sub>GS</sub> =18V, I <sub>S</sub> =180A	Tj=25°C	ı	2.3	-	
			Tj=125°C	1	3.3	-	
			Tj=150°C	ı	3.5	-	
Gate-source threshold voltage	$V_{GS(th)}$	$V_{DS}$ =10V, $I_{D}$ =35.2mA	1.6	2.7	4	V	
Gate-source leakage current	I <sub>GSS</sub>	$V_{GS}$ =22V, $V_{DS}$ =0V		ı	-	0.5	Δ
		$V_{GS}$ = -6V, $V_{DS}$ =0V		-0.5	-	-	μΑ
Switching characteristics	td(on)	$V_{GS(on)}$ =18V, $V_{GS(off)}$ =0V		-	80	-	ns
	tr	V <sub>DS</sub> =600V		-	90	-	
	trr	$I_D$ =180A $R_G$ =5.6Ω		-	50	-	
	td(off)			ı	300	-	
	tf	inductive load	1	90	-		
Input capacitance	Ciss	$V_{DS}$ =10V, $V_{GS}$ =0V, f=1MHz		ı	23	-	nF
Internal gate resistor	$R_{\text{Gint}}$	Tj=25°C	ı	1.15	-	Ω	
Stray Inductance	Ls			ı	25	-	nΗ
Creepage Distance	1	Terminal to heat sink		ı	11.5	-	mm
		Terminal to terminal		1	19.0	-	mm
Clearance Distance	ı	Terminal to heat sink		1	9.5	-	mm
		Terminal to terminal		ı	13.0	-	mm
Junction-to-case thermal resistance	Rth(j-c)	DMOS (1/2 module) *5		-	1	0.11	°C/W
Case-to-heat sink Thermal resistance	Rth(c-f)	Case to heat sink, per Thermal grease appie	-	0.035	-	C/VV	

- (\*4) In order to prevent self turn-on, it is recommended to apply negative gate bias.
- (\*5) Measurement of Tc is to be done at the point just under the chip.
- (\*6) Typical value is measured by using thermally conductive grease of λ=0.9W/(m • K).
- (\*7) SiC devices have lower short cuicuit withstand capability due to high current density. Please be advised to pay careful attention to short cuicuit accident and try to adjust protection time to shutdown them as short as possible.
- (\*8) If the Product is used beyond absolute maximum ratings defined in the Specifications, as its internal structure may be dameged, please replace such Product with a new one.

<Wavelength for Switching Test>

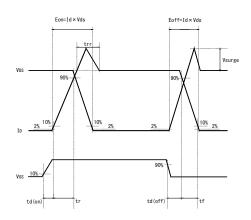
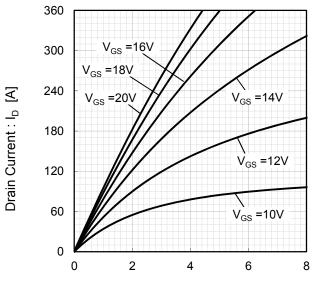


Fig.1 Typical Output Characteristics



Drain-Source Voltage : V<sub>DS</sub> [V]

Fig.2 Drain-Source Voltage vs. Drain Current

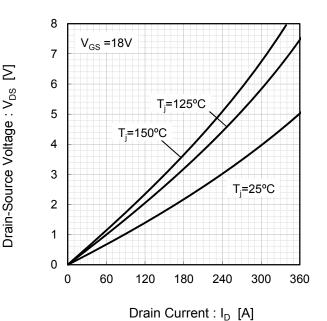
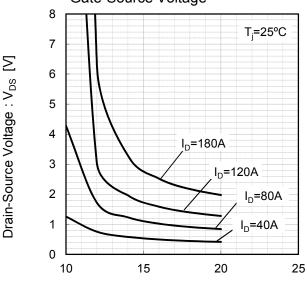
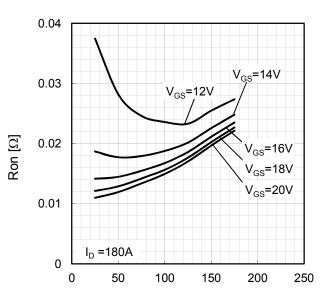


Fig.3 Drain-Source Voltage vs. Gate-Source Voltage



Gate-Source Voltage : V<sub>GS</sub> [V]

Fig.4 Ron vs Junction Temperature



Junction Temperature : Tj [°C]

Fig.5 Drain Current vs. Gate-Source Voltage

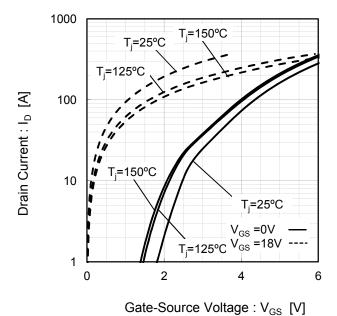
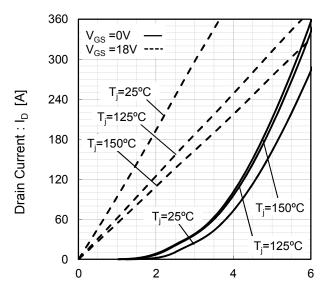


Fig.6 Drain Current vs. Gate-Source Voltage



Gate-Source Voltage : V<sub>GS</sub> [V]

Fig.7 Drain Current vs. Gate-Source Voltage

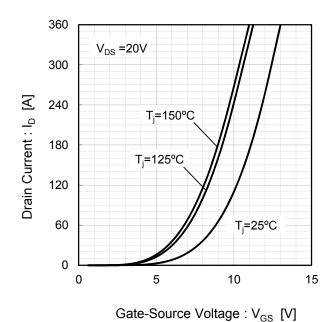
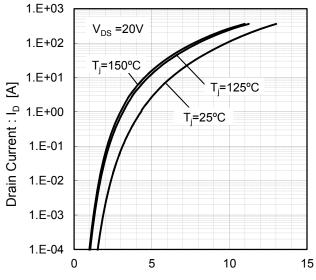


Fig.8 Drain Current vs. Gate-Source Voltage



Gate-Source Voltage : V<sub>GS</sub> [V]

Fig.9 Switching Characteristics [Tj=25°C]

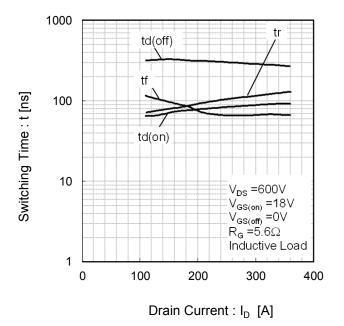


Fig.10 Switching Characteristics [Tj=125°C]

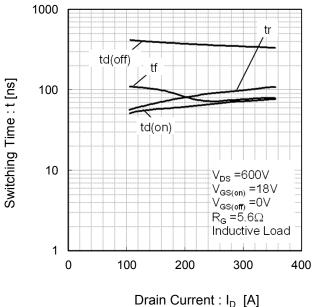


Fig.11 Switching Loss vs. Drain Current [ Tj=25°C ]

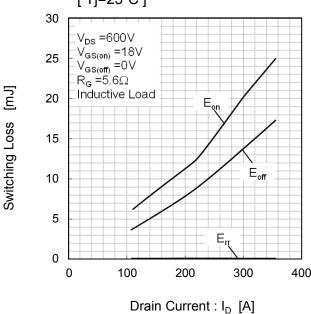
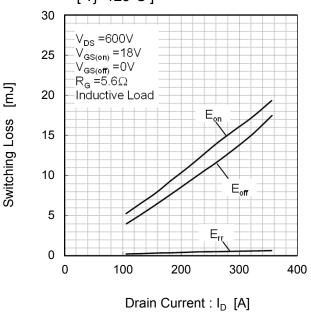
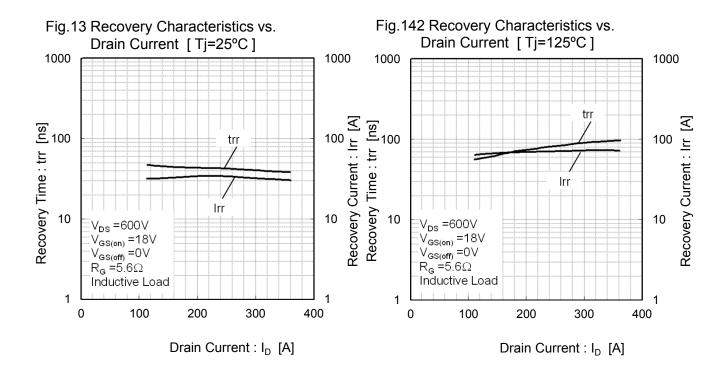


Fig.12 Switching Loss vs. Drain Current [Tj=125°C]

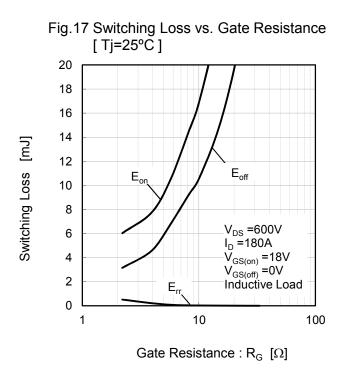




Resistance [Tj=125°C]

10000  $V_{DS} = 600V$   $I_{D} = 180A$   $V_{GS(on)} = 18V$   $V_{GS(of)} = 0V$  Inductive Load 100 10 10  $Gate Resistance : R_{G} [\Omega]$ 

Fig.16 Switching Characteristics vs. Gate



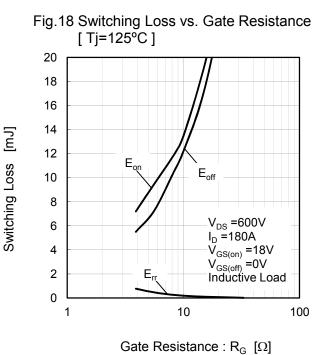


Fig.19 Typical Capacitance vs. Drain-Source Voltage 1.E-07  $C_{\text{iss}}$ 1.E-08 Capasitance : C [nF]  $\mathsf{C}_{\mathsf{oss}}$ 1.E-09 1.E-10  $\mathsf{C}_{\mathsf{rss}}$ Tj=25°C V<sub>GS</sub> =0V 1.E-11 1 100 0.01

Drain-Source Voltage : V<sub>DS</sub> [V]

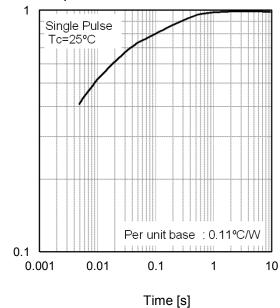
Gate-Source Voltage: V<sub>GS</sub> [V]

Fig.20 Gate Charge Characteristics [ Tj=25°C ] 25 I<sub>D</sub> =180A Tj=25°C 20 15 10 5 0 500 1000 0 1500

Total Gate charge : Qg [nC]

Fig.21 Normalized Transient Thermal Impedance





ROHM

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