



72V3611L20PF Information



For Reference Only

Part Number 72V3611L20PF

Manufacturer IDT, Integrated Device Technology Inc

Category Integrated Circuits (ICs) Logic - FIFOs Memory

Description IC FIFO SYNC 64X36 20NS 120-TQFP

Package 120-LQFF

For the pricing/inventory/lead time, please contact

us

Website: https://www.heisener.com E-mail: salesdept@heisener.com



Request a Quote

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









72V3611L20PF Specifications

| Manufacturer Part Number | 72V3611L20PF |
|----------------------------|---------------------------------------|
| Manufacturer | IDT, Integrated Device Technology Inc |
| Category | Integrated Circuits (ICs) |
| | Logic - FIFOs Memory |
| Package | 120-LQFP |
| Series | 72 V |
| Memory Size | 2.25K (64 x 36) |
| Function | Synchronous |
| Data Rate | 50MHz |
| Access Time | 12ns |
| Voltage - Supply | 3 V ~ 3.6 V |
| Current - Supply (Max) | - |
| Bus Directional | Bi-Directional |
| Expansion Type | Width |
| Programmable Flags Support | Yes |
| Retransmit Capability | No |
| FWFT Support | No |
| Operating Temperature | 0°C ~ 70°C |
| Mounting Type | Surface Mount |
| Package / Case | 120-LQFP |
| Supplier Device Package | 120-TQFP (14x14) |
| | Report errors? |

72V3611L20PF Guarantees



Quality Guarantees

We provide 90 days warranty. *

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

72V3611L20PF Payment Methods



















72V3611L20PF Shipping Methods













If you have any question about 72V3611L20PF, please do not hesitate to contact us!

Website: https://www.heisener.com E-mail: salesdept@heisener.com