

**72V81L20PA Information**


For Reference Only

**Part Number** [72V81L20PA](#)  
**Manufacturer** IDT, Integrated Device Technology Inc  
**Category** Integrated Circuits (ICs)  
[Logic - FIFOs Memory](#)  
**Description** IC FIFO ASYNCH 512X9 56TSSOP  
**Package** 56-TFSOP (0.240", 6.10mm Width)  
 For the pricing/inventory/lead time, please contact us  
 Website: <https://www.heisener.com>  
 E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)


[Request a Quote](#)
**Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


**72V81L20PA Specifications**

Manufacturer Part Number	<a href="#">72V81L20PA</a>
Manufacturer	IDT, Integrated Device Technology Inc
Category	Integrated Circuits (ICs) <a href="#">Logic - FIFOs Memory</a>
Package	56-TFSOP (0.240", 6.10mm Width)
Series	72V
Memory Size	9K (512 x 9 x 2)
Function	Asynchronous
Data Rate	33.3MHz
Access Time	20ns
Voltage - Supply	3 V ~ 3.6 V
Current - Supply (Max)	100mA
Bus Directional	Uni-Directional
Expansion Type	Depth, Width
Programmable Flags Support	No
Retransmit Capability	Yes
FWFT Support	No
Operating Temperature	0°C ~ 70°C
Mounting Type	Surface Mount
Package / Case	56-TFSOP (0.240", 6.10mm Width)
Supplier Device Package	56-TSSOP

[Report errors?](#)

## 72V81L20PA Guarantees



### Quality Guarantees

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



### Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

## 72V81L20PA Payment Methods



## 72V81L20PA Shipping Methods



If you have any question about 72V81L20PA, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)