



74AHC02PW,112 Information



For Reference Only

Part Number 74AHC02PW,112

Manufacturer Nexperia USA Inc.

Category Integrated Circuits (ICs)
Logic - Gates and Inverters

Description IC GATE NOR 4CH 2-INP 14-TSSOP **Package** 14-TSSOP (0.173", 4.40mm Width)

For the pricing/inventory/lead time, please contact

us

Website: https://www.heisener.com E-mail: salesdept@heisener.com



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74AHC02PW,112 Specifications

Manufacturer Part Number 74AHC02PW,112 Manufacturer Nexperia USA Inc. Category Integrated Circuits (ICs) Logic - Gates and Inverters Package 14-TSSOP (0.173", 4.40mm Width) Series 74AHC Logic Type NOR Gate Number of Circuits 4 Number of Inputs 2 Features - Voltage - Supply 2 V ~ 5.5 V Current - Quiescent (Max) 2 μA Current - Output High, Low 8mA, 8mA Logic Level - Low 0.5 V ~ 1.65 V Logic Level - High 1.5 V ~ 3.85 V Max Propagation Delay @ V, Max CL 7.5ns @ 5V, 50pF Operating Temperature -40°C ~ 125°C Mounting Type Surface Mount Supplier Device Package 14-TSSOP Package / Case 14-TSSOP (0.173", 4.40mm Width)		
CategoryIntegrated Circuits (ICs)Logic - Gates and InvertersPackage 14 -TSSOP (0.173 ", 4.40 mm Width)Series 74 AHCLogic TypeNOR GateNumber of Circuits 4 Number of Inputs 2 Features $-$ Voltage - Supply $2 \text{ V} \sim 5.5 \text{ V}$ Current - Quiescent (Max) $2\mu\text{A}$ Current - Output High, Low 8mA , 8mA Logic Level - Low $0.5 \text{ V} \sim 1.65 \text{ V}$ Logic Level - High $1.5 \text{ V} \sim 3.85 \text{ V}$ Max Propagation Delay @ V, Max CL 7.5ns @ 5V , 50pF Operating Temperature $-40^{\circ}\text{C} \sim 125^{\circ}\text{C}$ Mounting TypeSurface MountSupplier Device Package 14-TSSOP	Manufacturer Part Number	74AHC02PW,112
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Series $74AHC$ Logic TypeNOR GateNumber of Circuits4Number of Inputs2Features-Voltage - Supply $2 \text{ V} \sim 5.5 \text{ V}$ Current - Quiescent (Max) $2\mu A$ Current - Output High, Low $8mA$, $8mA$ Logic Level - Low $0.5 \text{ V} \sim 1.65 \text{ V}$ Logic Level - High $1.5 \text{ V} \sim 3.85 \text{ V}$ Max Propagation Delay @ V, Max CL $7.5ns$ @ $5V$, $50pF$ Operating Temperature $-40^{\circ}\text{C} \sim 125^{\circ}\text{C}$ Mounting TypeSurface MountSupplier Device Package 14-TSSOP		Logic - Gates and Inverters
Logic TypeNOR GateNumber of Circuits4Number of Inputs2Features-Voltage - Supply $2 \text{ V} \sim 5.5 \text{ V}$ Current - Quiescent (Max) $2\mu A$ Current - Output High, Low $8mA, 8mA$ Logic Level - Low $0.5 \text{ V} \sim 1.65 \text{ V}$ Logic Level - High $1.5 \text{ V} \sim 3.85 \text{ V}$ Max Propagation Delay @ V, Max CL 7.5ns @ 5V , 50pF Operating Temperature $-40^{\circ}\text{C} \sim 125^{\circ}\text{C}$ Mounting TypeSurface MountSupplier Device Package 14-TSSOP	Package	14-TSSOP (0.173", 4.40mm Width)
Number of Circuits4Number of Inputs2Features-Voltage - Supply2 V ~ 5.5 VCurrent - Quiescent (Max)2μACurrent - Output High, Low8mA, 8mALogic Level - Low0.5 V ~ 1.65 VLogic Level - High1.5 V ~ 3.85 VMax Propagation Delay @ V, Max CL7.5ns @ 5V, 50pFOperating Temperature-40°C ~ 125°CMounting TypeSurface MountSupplier Device Package14-TSSOP	Series	74AHC
Number of Inputs 2 Features - Voltage - Supply 2 $V \sim 5.5 V$ Current - Quiescent (Max) 2 μ A Current - Output High, Low 8 μ A, 8 μ A Logic Level - Low 0.5 $V \sim 1.65 V$ Logic Level - High 1.5 $V \sim 3.85 V$ Max Propagation Delay @ V, Max CL 7.5 μ S of SV, 50pF Operating Temperature -40°C ~ 125°C Mounting Type Surface Mount Supplier Device Package 14-TSSOP	Logic Type	NOR Gate
Features $ \begin{array}{ccccccccccccccccccccccccccccccccccc$	Number of Circuits	4
Voltage - Supply $2\ V \sim 5.5\ V$ $Current - Quiescent (Max) 2\mu A Current - Output High, Low 8mA, 8mA Logic Level - Low \\ Logic Level - High \\ Max \ Propagation Delay @ V, Max \ CL \\ Operating Temperature -40^{\circ}C \sim 125^{\circ}C Mounting \ Type Surface \ Mount \\ Supplier \ Device \ Package 14-TSSOP$	Number of Inputs	2
Current - Quiescent (Max) $2\mu A$ $8mA$, $8mA$ $105 \text{ V} \sim 1.65 \text{ V}$ $1.5 \text{ V} \sim 3.85 \text{ V}$	Features	-
Current - Output High, Low $8mA$, $8mA$ Logic Level - Low $0.5 \text{ V} \sim 1.65 \text{ V}$ Logic Level - High $1.5 \text{ V} \sim 3.85 \text{ V}$ Max Propagation Delay @ V, Max CL 7.5ns @ 5V , 50pF Operating Temperature $-40^{\circ}\text{C} \sim 125^{\circ}\text{C}$ Mounting TypeSurface MountSupplier Device Package 14-TSSOP	Voltage - Supply	2 V ~ 5.5 V
Logic Level - Low $0.5 \text{ V} \sim 1.65 \text{ V}$ Logic Level - High $1.5 \text{ V} \sim 3.85 \text{ V}$ Max Propagation Delay @ V, Max CL $7.5 \text{ns} \otimes 5 \text{V}, 50 \text{pF}$ Operating Temperature $-40^{\circ}\text{C} \sim 125^{\circ}\text{C}$ Mounting TypeSurface MountSupplier Device Package 14-TSSOP	Current - Quiescent (Max)	2μΑ
Logic Level - High $1.5 \text{ V} \sim 3.85 \text{ V}$ Max Propagation Delay @ V, Max CL 7.5ns @ 5V , 50pF Operating Temperature $-40 ^{\circ}\text{C} \sim 125 ^{\circ}\text{C}$ Mounting TypeSurface MountSupplier Device Package 14-TSSOP	Current - Output High, Low	8mA, 8mA
Max Propagation Delay @ V, Max CL7.5ns @ 5V, 50pFOperating Temperature-40°C ~ 125°CMounting TypeSurface MountSupplier Device Package14-TSSOP	Logic Level - Low	0.5 V ~ 1.65 V
Operating Temperature -40°C ~ 125°C Mounting Type Surface Mount Supplier Device Package 14-TSSOP	Logic Level - High	1.5 V ~ 3.85 V
Mounting Type Surface Mount Supplier Device Package 14-TSSOP	Max Propagation Delay @ V, Max CL	7.5ns @ 5V, 50pF
Supplier Device Package 14-TSSOP	Operating Temperature	-40°C ~ 125°C
	Mounting Type	Surface Mount
Package / Case 14-TSSOP (0.173", 4.40mm Width)	Supplier Device Package	14-TSSOP
	Package / Case	14-TSSOP (0.173", 4.40mm Width)
Report errors?		Report errors?

74AHC02PW,112 Guarantees



Quality Guarantees

We provide 90 days warranty. *

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

74AHC02PW,112 Payment Methods



















74AHC02PW,112 Shipping Methods













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