

74F112SJX Information


For Reference Only

Part Number 74F112SJX
Manufacturer Fairchild/ON Semiconductor
Category Integrated Circuits (ICs)
[Logic - Flip Flops](#)
Description IC JK TYPE NEG TRG DUAL 16SOP
Package 16-SOIC (0.209", 5.30mm Width)
 For the pricing/inventory/lead time, please contact us
 Website: <https://www.heisener.com>
 E-mail: salesdept@heisener.com


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Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


74F112SJX Specifications

Manufacturer Part Number	74F112SJX
Manufacturer	Fairchild/ON Semiconductor
Category	Integrated Circuits (ICs) Logic - Flip Flops
Package	16-SOIC (0.209", 5.30mm Width)
Series	74F
Function	Set(Preset) and Reset
Type	JK Type
Output Type	Differential
Number of Elements	2
Number of Bits per Element	1
Clock Frequency	105MHz
Max Propagation Delay @ V, Max CL	6.5ns @ 5V, 50pF
Trigger Type	Negative Edge
Current - Output High, Low	1mA, 20mA
Voltage - Supply	4.5 V ~ 5.5 V
Current - Quiescent (Iq)	19mA
Input Capacitance	-
Operating Temperature	0°C ~ 70°C (TA)
Mounting Type	Surface Mount
Package / Case	16-SOIC (0.209", 5.30mm Width)

[Report errors?](#)

74F112SJX Guarantees



Quality Guarantees

We provide 90 days warranty. *

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

74F112SJX Payment Methods



74F112SJX Shipping Methods



If you have any question about 74F112SJX, please do not hesitate to contact us!

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