

74FCT162823ATPAG8

74FCT162823ATPAG8 Information



For Reference Only

Part Number 74FCT162823ATPAG8

Manufacturer IDT, Integrated Device Technology Inc

Category Integrated Circuits (ICs)

Logic - Flip Flops

Description IC D-TYPE POS TRG DUAL 56TSSOP

Package 56-TFSOP (0.240", 6.10mm Width)

For the pricing/inventory/lead time, please contact

Website: https://www.heisener.com E-mail: salesdept@heisener.com



Request a Quote

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









74FCT162823ATPAG8 Specifications

Manufacturer Part Number	74FCT162823ATPAG8
Manufacturer	IDT, Integrated Device Technology Inc
Category	Integrated Circuits (ICs)
	Logic - Flip Flops
Package	56-TFSOP (0.240", 6.10mm Width)
Series	74FCT
Function	Master Reset
Туре	D-Type
Output Type	Tri-State, Non-Inverted
Number of Elements	2
Number of Bits per Element	9
Clock Frequency	-
Max Propagation Delay @ V, Max CL	20ns @ 5V, 300pF
Trigger Type	Positive Edge
Current - Output High, Low	24mA, 24mA
Voltage - Supply	4.5 V ~ 5.5 V
Current - Quiescent (Iq)	500μΑ
Input Capacitance	3.5pF
Operating Temperature	-40°C ~ 85°C (TA)
Mounting Type	Surface Mount
Package / Case	56-TFSOP (0.240", 6.10mm Width)
	Report errors?

74FCT162823ATPAG8 Guarantees



Ouality Guarantees

We provide 90 days warranty. *

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

74FCT162823ATPAG8 Payment Methods



















74FCT162823ATPAG8 Shipping Methods













If you have any question about 74FCT162823ATPAG8, please do not hesitate to contact us!

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