

74HC4353D,652

Quote

74HC4353D,652 Information

www.belsener.com	Part Number	74HC4353D,652	
	Manufacturer	NXP	
	Category	Integrated Circuits (ICs) Interface - Analog Switches, Multiplexers, Demultiplexers	- 200
	Description	IC MUX/DEMUX TRIPLE 1X2 20SOIC	2.33.6
	Package	20-SOIC (0.295", 7.50mm Width)	_ m3%
For Reference Only		For the pricing/inventory/lead time, please contact us Website: https://www.heisener.com E-mail: salesdept@heisener.com	Request a C

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



74HC4353D,652 Specifications

Manufacturer Part Number	74HC4353D,652
Manufacturer	NXP
Category	Integrated Circuits (ICs)
	Interface - Analog Switches, Multiplexers, Demultiplexers
Package	20-SOIC (0.295", 7.50mm Width)
Series	-
Switch Circuit	SPDT
Multiplexer/Demultiplexer Circuit	2:1
Number of Circuits	3
On-State Resistance (Max)	120 Ohm
Channel-to-Channel Matching (Ron)	6 Ohm
Voltage - Supply, Single (V+)	2 V ~ 10 V
Voltage - Supply, Dual (V±)	±1 V ~ 5 V
Switch Time (Ton, Toff) (Max)	40ns, 40ns
-3db Bandwidth	170MHz
Charge Injection	-
Channel Capacitance (CS(off), CD(off))	3.5pF
Current - Leakage (IS(off)) (Max)	100nA
Crosstalk	-60dB @ 1MHz
Operating Temperature	-40°C ~ 125°C (TA)
Package / Case	20-SOIC (0.295", 7.50mm Width)
Supplier Device Package	20-SO
	Report errors?

74HC4353D,652 Guarantees



Quality Guarantees

We provide 90 days warranty. * If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.

SERVICE

Service Guarantees

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

74HC4353D,652 Payment Methods





If you have any question about 74HC4353D,652, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com