

### 74LV02PW,112 Information



For Reference Only

Part Number 74LV02PW,112

Manufacturer Nexperia USA Inc.

Category Integrated Circuits (ICs)
Logic - Gates and Inverters

**Description** IC GATE NOR 4CH 2-INP 14-TSSOP **Package** 14-TSSOP (0.173", 4.40mm Width)

For the pricing/inventory/lead time, please contact

us

Website: https://www.heisener.com E-mail: salesdept@heisener.com



Request a Quote

# **Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









## 74LV02PW,112 Specifications

Manufacturer Part Number         74LV02PW,112           Manufacturer         Nexperia USA Inc.           Category         Integrated Circuits (ICs)           Logic - Gates and Inverters           Package         14-TSSOP (0.173", 4.40mm Width)           Series         74LV           Logic Type         NOR Gate           Number of Circuits         4           Number of Inputs         2           Features         -           Voltage - Supply         1 V ~ 5.5 V           Current - Quiescent (Max)         40μA           Current - Output High, Low         12mA, 12mA           Logic Level - Low         0.3 V ~ 0.8 V           Logic Level - High         0.9 V ~ 2 V           Max Propagation Delay @ V, Max CL         6ns @ 5V, 50pF           Operating Temperature         -40°C ~ 125°C           Mounting Type         Surface Mount		
Category         Integrated Circuits (ICs)           Logic - Gates and Inverters           Package         14-TSSOP (0.173", 4.40mm Width)           Series         74LV           Logic Type         NOR Gate           Number of Circuits         4           Number of Inputs         2           Features         -           Voltage - Supply         1 V ~ 5.5 V           Current - Quiescent (Max)         40μA           Current - Output High, Low         12mA, 12mA           Logic Level - Low         0.3 V ~ 0.8 V           Logic Level - High         0.9 V ~ 2 V           Max Propagation Delay @ V, Max CL         6ns @ 5V, 50pF           Operating Temperature         -40°C ~ 125°C	Manufacturer Part Number	74LV02PW,112
$\begin{array}{c} Logic - Gates \ and \ Inverters \\ \\ Package & 14-TSSOP \ (0.173", 4.40mm \ Width) \\ \\ Series & 74LV \\ \\ Logic Type & NOR Gate \\ \\ Number of Circuits & 4 \\ \\ Number of Inputs & 2 \\ \\ Features & - \\ \\ Voltage - Supply & 1 \ V \sim 5.5 \ V \\ \\ Current - Quiescent \ (Max) & 40\mu A \\ \\ Current - Output \ High, Low & 12mA, 12mA \\ \\ Logic \ Level - Low & 0.3 \ V \sim 0.8 \ V \\ \\ Logic \ Level - High & 0.9 \ V \sim 2 \ V \\ \\ Max \ Propagation \ Delay @ V, Max \ CL & 6ns \ @ 5V, 50pF \\ \\ Operating \ Temperature & -40^{\circ}C \sim 125^{\circ}C \\ \end{array}$	Manufacturer	Nexperia USA Inc.
Package 14-TSSOP (0.173", 4.40mm Width) Series 74LV  Logic Type NOR Gate  Number of Circuits 4  Number of Inputs 2  Features - Voltage - Supply 1 $V \sim 5.5 V$ Current - Quiescent (Max) 40 $\mu$ A  Current - Output High, Low 12 $\mu$ A, 12 $\mu$ A  Logic Level - Low 0.3 $V \sim 0.8 V$ Logic Level - High 0.9 $V \sim 2 V$ Max Propagation Delay @ V, Max CL 6ns @ 5V, 50pF  Operating Temperature -40°C ~ 125°C	Category	Integrated Circuits (ICs)
Series       74L V         Logic Type       NOR Gate         Number of Circuits       4         Number of Inputs       2         Features       -         Voltage - Supply       1 V ~ 5.5 V         Current - Quiescent (Max)       40μA         Current - Output High, Low       12mA, 12mA         Logic Level - Low       0.3 V ~ 0.8 V         Logic Level - High       0.9 V ~ 2 V         Max Propagation Delay @ V, Max CL       6ns @ 5V, 50pF         Operating Temperature       -40°C ~ 125°C		Logic - Gates and Inverters
Logic TypeNOR GateNumber of Circuits4Number of Inputs2Features-Voltage - Supply $1 \text{ V} \sim 5.5 \text{ V}$ Current - Quiescent (Max) $40\mu\text{A}$ Current - Output High, Low $12\text{mA}$ , $12\text{mA}$ Logic Level - Low $0.3 \text{ V} \sim 0.8 \text{ V}$ Logic Level - High $0.9 \text{ V} \sim 2 \text{ V}$ Max Propagation Delay @ V, Max CL $6\text{ns @ 5V, 50pF}$ Operating Temperature $-40^{\circ}\text{C} \sim 125^{\circ}\text{C}$	Package	14-TSSOP (0.173", 4.40mm Width)
Number of Circuits4Number of Inputs2Features-Voltage - Supply $1 \text{ V} \sim 5.5 \text{ V}$ Current - Quiescent (Max) $40\mu\text{A}$ Current - Output High, Low $12\text{mA}$ , $12\text{mA}$ Logic Level - Low $0.3 \text{ V} \sim 0.8 \text{ V}$ Logic Level - High $0.9 \text{ V} \sim 2 \text{ V}$ Max Propagation Delay @ V, Max CL $6\text{ns}$ @ $5\text{V}$ , $50\text{pF}$ Operating Temperature $-40^{\circ}\text{C} \sim 125^{\circ}\text{C}$	Series	74LV
Number of Inputs2Features-Voltage - Supply $1 \text{ V} \sim 5.5 \text{ V}$ Current - Quiescent (Max) $40\mu\text{A}$ Current - Output High, Low $12\text{mA}$ , $12\text{mA}$ Logic Level - Low $0.3 \text{ V} \sim 0.8 \text{ V}$ Logic Level - High $0.9 \text{ V} \sim 2 \text{ V}$ Max Propagation Delay @ V, Max CL $6\text{ns}$ @ $5\text{V}$ , $50\text{pF}$ Operating Temperature $-40^{\circ}\text{C} \sim 125^{\circ}\text{C}$	Logic Type	NOR Gate
$ \begin{array}{llllllllllllllllllllllllllllllllllll$	Number of Circuits	4
Voltage - Supply $1\ V \sim 5.5\ V$ Current - Quiescent (Max) $40\mu A$ Current - Output High, Low $12mA, 12mA$ Logic Level - Low $0.3\ V \sim 0.8\ V$ Logic Level - High $0.9\ V \sim 2\ V$ Max Propagation Delay @ V, Max CL $6ns\ @ 5V, 50pF$ Operating Temperature $-40^{\circ}C \sim 125^{\circ}C$	Number of Inputs	2
Current - Quiescent (Max) $40\mu A$ Current - Output High, Low $12mA$ , $12mA$ Logic Level - Low $0.3 \text{ V} \sim 0.8 \text{ V}$ Logic Level - High $0.9 \text{ V} \sim 2 \text{ V}$ Max Propagation Delay @ V, Max CL $6ns @ 5V$ , $50pF$ Operating Temperature $-40^{\circ}C \sim 125^{\circ}C$	Features	-
Current - Output High, Low $12mA$ , $12mA$ Logic Level - Low $0.3 \text{ V} \sim 0.8 \text{ V}$ Logic Level - High $0.9 \text{ V} \sim 2 \text{ V}$ Max Propagation Delay @ V, Max CL $6ns @ 5V$ , $50pF$ Operating Temperature $-40^{\circ}\text{C} \sim 125^{\circ}\text{C}$	Voltage - Supply	1 V ~ 5.5 V
Logic Level - Low $0.3 \text{ V} \sim 0.8 \text{ V}$ Logic Level - High $0.9 \text{ V} \sim 2 \text{ V}$ Max Propagation Delay @ V, Max CL $6\text{ns} \ \text{@} 5\text{V}, 50\text{pF}$ Operating Temperature $-40^{\circ}\text{C} \sim 125^{\circ}\text{C}$	Current - Quiescent (Max)	40μΑ
Logic Level - High $0.9 \text{ V} \sim 2 \text{ V}$ Max Propagation Delay @ V, Max CL $6ns @ 5V, 50pF$ Operating Temperature $-40^{\circ}C \sim 125^{\circ}C$	Current - Output High, Low	12mA, 12mA
Max Propagation Delay @ V, Max CL  6ns @ 5V, 50pF  Operating Temperature  -40°C ~ 125°C	Logic Level - Low	0.3 V ~ 0.8 V
Operating Temperature $-40^{\circ}\text{C} \sim 125^{\circ}\text{C}$	Logic Level - High	0.9 V ~ 2 V
	Max Propagation Delay @ V, Max CL	6ns @ 5V, 50pF
Mounting Type Surface Mount	Operating Temperature	-40°C ~ 125°C
	Mounting Type	Surface Mount
Supplier Device Package 14-TSSOP	Supplier Device Package	14-TSSOP
Package / Case 14-TSSOP (0.173", 4.40mm Width)	Package / Case	14-TSSOP (0.173", 4.40mm Width)
Report errors?		Report errors?

### 74LV02PW,112 Guarantees



### **Quality Guarantees**

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



#### **Service Guarantees**

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

### 74LV02PW,112 Payment Methods



















## 74LV02PW,112 Shipping Methods













If you have any question about 74LV02PW,112, please do not hesitate to contact us!

Website: https://www.heisener.com E-mail: salesdept@heisener.com