



74LVC86ADTR2G Information



For Reference Only

Part Number 74LVC86ADTR2G

Manufacturer ON Semiconductor

Category Integrated Circuits (ICs)
Logic - Gates and Inverters

Description IC GATE XOR QUAD 2INPUT 14TSSOP

Package 14-TSSOP (0.173", 4.40mm Width)

For the pricing/inventory/lead time, please contact

us

Website: https://www.heisener.com E-mail: salesdept@heisener.com



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74LVC86ADTR2G Specifications

Manufacturer Part Number 74LVC86ADTR2G Manufacturer ON Semiconductor Category Integrated Circuits (ICs) Logic - Gates and Inverters Logic - Gates and Inverters Package 14-TSSOP (0.173", 4.40mm Width) Series 74LVC Logic Type XOR (Exclusive OR) Number of Circuits 4 Number of Inputs 2 Features - Voltage - Supply 1.2 V ~ 3.6 V Current - Quiescent (Max) 40μA Current - Output High, Low 24mA, 24mA Logic Level - Low 0.12 V ~ 0.8 V Logic Level - High 1.08 V ~ 2 V Max Propagation Delay @ V, Max CL 6ns @ 3.3V, 50pF Operating Temperature -40°C ~ 125°C Mounting Type Surface Mount Supplier Device Package 14-TSSOP Package / Case 14-TSSOP (0.173", 4.40mm Width)		
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Voltage - Supply $1.2 \text{ V} \sim 3.6 \text{ V}$ Current - Quiescent (Max) $40\mu\text{A}$ Current - Output High, Low $24\text{mA}, 24\text{mA}$ Logic Level - Low $0.12 \text{ V} \sim 0.8 \text{ V}$ Logic Level - High $1.08 \text{ V} \sim 2 \text{ V}$ Max Propagation Delay @ V, Max CL $6\text{ns @ } 3.3\text{V}, 50\text{pF}$ Operating Temperature $-40^{\circ}\text{C} \sim 125^{\circ}\text{C}$ Mounting Type Surface Mount Supplier Device Package 14-TSSOP Package / Case $14\text{-TSSOP} (0.173'', 4.40\text{mm Width})$	Number of Inputs	2
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Logic Level - Low $0.12 \text{ V} \sim 0.8 \text{ V}$ Logic Level - High $1.08 \text{ V} \sim 2 \text{ V}$ Max Propagation Delay @ V, Max CL $6ns @ 3.3V, 50pF$ Operating Temperature $-40^{\circ}\text{C} \sim 125^{\circ}\text{C}$ Mounting TypeSurface MountSupplier Device Package 14-TSSOP Package / Case $14\text{-TSSOP} (0.173", 4.40mm \text{ Width})$	Current - Quiescent (Max)	40μΑ
Logic Level - High 1.08 V ~ 2 V Max Propagation Delay @ V, Max CL 6ns @ 3.3V, 50pF Operating Temperature -40°C ~ 125°C Mounting Type Surface Mount Supplier Device Package 14-TSSOP Package / Case 14-TSSOP (0.173", 4.40mm Width)	Current - Output High, Low	24mA, 24mA
Max Propagation Delay @ V, Max CL 6ns @ 3.3V, 50pF Operating Temperature -40°C ~ 125°C Mounting Type Surface Mount Supplier Device Package 14-TSSOP Package / Case 14-TSSOP (0.173", 4.40mm Width)	Logic Level - Low	$0.12 \text{ V} \sim 0.8 \text{ V}$
Operating Temperature -40°C ~ 125°C Mounting Type Surface Mount Supplier Device Package 14-TSSOP Package / Case 14-TSSOP (0.173", 4.40mm Width)	Logic Level - High	1.08 V ~ 2 V
Mounting Type Surface Mount Supplier Device Package 14-TSSOP Package / Case 14-TSSOP (0.173", 4.40mm Width)	Max Propagation Delay @ V, Max CL	6ns @ 3.3V, 50pF
Supplier Device Package 14-TSSOP Package / Case 14-TSSOP (0.173", 4.40mm Width)	Operating Temperature	-40°C ~ 125°C
Package / Case 14-TSSOP (0.173", 4.40mm Width)	Mounting Type	Surface Mount
	Supplier Device Package	14-TSSOP
	Package / Case	14-TSSOP (0.173", 4.40mm Width)
Report errors?		Report errors?

74LVC86ADTR2G Guarantees



Quality Guarantees

We provide 90 days warranty. *

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

74LVC86ADTR2G Payment Methods





















74LVC86ADTR2G Shipping Methods













If you have any question about 74LVC86ADTR2G, please do not hesitate to contact us!

Website: https://www.heisener.com E-mail: salesdept@heisener.com