

# 74LVT574PW/AUJ

#### 74LVT574PW/AUJ Information

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For Reference Only		us Website: https://www.heisener.com E-mail: salesdept@heisener.com	Request a Quote

# **Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



# 74LVT574PW/AUJ Specifications

Manufacturer Part Number	74LVT574PW/AUJ
Manufacturer	NXP
Category	Integrated Circuits (ICs)
	Logic - Flip Flops
Package	20-TSSOP (0.173", 4.40mm Width)
Series	74LVT
Function	Standard
Туре	D-Type
Output Type	Tri-State, Non-Inverted
Number of Elements	1
Number of Bits per Element	8
Clock Frequency	150MHz
Max Propagation Delay @ V, Max CL	5.9ns @ 3.3V, 50pF
Trigger Type	Positive Edge
Current - Output High, Low	32mA, 64mA
Voltage - Supply	2.7 V ~ 3.6 V
Current - Quiescent (Iq)	190µA
Input Capacitance	4pF
Operating Temperature	-40°C ~ 85°C (TA)
Mounting Type	Surface Mount
Package / Case	20-TSSOP (0.173", 4.40mm Width)
	Report errors?

#### 74LVT574PW/AUJ Guarantees



Quality Guarantees

We provide 90 days warranty. \* If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.

# SERVICE BUARANTEE

#### **Service Guarantees**

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

# 74LVT574PW/AUJ Payment Methods



### 74LVT574PW/AUJ Shipping Methods



If you have any question about 74LVT574PW/AUJ, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com