

**74LVTH16244MTD Information**


For Reference Only

**Part Number** [74LVTH16244MTD](#)  
**Manufacturer** Fairchild/ON Semiconductor  
**Category** Integrated Circuits (ICs)  
[Logic - Buffers, Drivers, Receivers, Transceivers](#)  
**Description** IC BUFF DVR TRI-ST 16BIT 48TSSOP  
**Package** 48-TFSOP (0.240", 6.10mm Width)  
 For the pricing/inventory/lead time, please contact us  
 Website: <https://www.heisener.com>  
 E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)


[Request a Quote](#)
**Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


**74LVTH16244MTD Specifications**

Manufacturer Part Number	<a href="#">74LVTH16244MTD</a>
Manufacturer	Fairchild/ON Semiconductor
Category	Integrated Circuits (ICs) <a href="#">Logic - Buffers, Drivers, Receivers, Transceivers</a>
Package	48-TFSOP (0.240", 6.10mm Width)
Series	74LVTH
Logic Type	Buffer, Non-Inverting
Number of Elements	4
Number of Bits per Element	4
Input Type	-
Output Type	Push-Pull
Current - Output High, Low	32mA, 64mA
Voltage - Supply	2.7 V ~ 3.6 V
Operating Temperature	-40°C ~ 85°C (TA)
Mounting Type	Surface Mount
Package / Case	48-TFSOP (0.240", 6.10mm Width)
Supplier Device Package	48-TSSOP

[Report errors?](#)

## 74LVTH16244MTD Guarantees



### Quality Guarantees

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



### Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

## 74LVTH16244MTD Payment Methods



## 74LVTH16244MTD Shipping Methods



If you have any question about 74LVTH16244MTD, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)