



# 82V3280EQG Information



For Reference Only

Part Number 82V3280EQG

Manufacturer IDT, Integrated Device Technology Inc

Category Integrated Circuits (ICs)

Clock/Timing - Application Specific

**Description** IC PLL WAN SE STRATUM 2 100TQFP

Package 100-LQFP Exposed Pad

For the pricing/inventory/lead time, please contact

us

Website: https://www.heisener.com E-mail: salesdept@heisener.com



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## **Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









### 82V3280EQG Specifications

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Manufacturer Part Number	82V3280EQG
Manufacturer	IDT, Integrated Device Technology Inc
Category	Integrated Circuits (ICs)
	Clock/Timing - Application Specific
Package	100-LQFP Exposed Pad
Series	-
PLL	Yes
Main Purpose	Ethernet, SONET/SDH, Stratum
Input	CMOS, LVDS, PECL
Output	CMOS, LVDS, PECL
Number of Circuits	1
Ratio - Input:Output	14:9
Differential - Input:Output	Yes/Yes
Frequency - Max	622.08MHz
Voltage - Supply	3 V ~ 3.6 V
Operating Temperature	-40°C ~ 85°C
Mounting Type	Surface Mount
Package / Case	100-LQFP Exposed Pad
Supplier Device Package	100-TQFP (14x14)
	Report errors?

### 82V3280EQG Guarantees



### **Quality Guarantees**

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



#### **Service Guarantees**

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

### 82V3280EQG Payment Methods



















### 82V3280EQG Shipping Methods













If you have any question about 82V3280EQG, please do not hesitate to contact us!

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