



82V3352EDG Information

Heisener.com

Part Number 82V3352EDG

Manufacturer IDT, Integrated Device Technology Inc

Integrated Circuits (ICs) Category

Clock/Timing - Application Specific

Description IC TIMING

Package 64-TQFP Exposed Pad

For the pricing/inventory/lead time, please contact

Website: https://www.heisener.com For Reference Only

E-mail: salesdept@heisener.com



Request a Quote

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









82V3352EDG Specifications

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Manufacturer Part Number	82V3352EDG
Manufacturer	IDT, Integrated Device Technology Inc
Category	Integrated Circuits (ICs)
	Clock/Timing - Application Specific
Package	64-TQFP Exposed Pad
Series	-
PLL	Yes
Main Purpose	Ethernet, SONET/SDH, Stratum
Input	CMOS, LVDS, PECL
Output	CMOS, LVDS, PECL
Number of Circuits	1
Ratio - Input:Output	3:2
Differential - Input:Output	Yes/Yes
Frequency - Max	622.08MHz
Voltage - Supply	3V ~ 3.6V
Operating Temperature	-40°C ~ 85°C (TA)
Mounting Type	Surface Mount
Package / Case	64-TQFP Exposed Pad
Supplier Device Package	64-TQFP (10x10)
	Report errors?

82V3352EDG Guarantees



Quality Guarantees

We provide 90 days warranty. *

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

82V3352EDG Payment Methods





















82V3352EDG Shipping Methods













If you have any question about 82V3352EDG, please do not hesitate to contact us!

Website: https://www.heisener.com E-mail: salesdept@heisener.com