

840021AGLF

840021AGLF Information

	Part Number	840021AGLF	
www.bersener.com	I al t Mullibel	040021A0LI	
	Manufacturer	IDT, Integrated Device Technology Inc	EN 259. EN
	Category	Integrated Circuits (ICs) Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers	
	Description	IC CLOCK GENERATOR 8-TSSOP	180 (161)
	Package	8-TSSOP (0.173", 4.40mm Width)	
		For the pricing/inventory/lead time, please contact	
For Reference Only		us Website: https://www.heisener.com	Request a Quote

E-mail: salesdept@heisener.com

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



840021AGLF Specifications

Manufacturer Part Number	840021AGLF	
Manufacturer	IDT, Integrated Device Technology Inc	
Category	Integrated Circuits (ICs)	
	Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers	
Package	8-TSSOP (0.173", 4.40mm Width)	
Series	FemtoClock?	
Туре	Clock/Frequency Synthesizer, Clock Generator	
PLL	Yes	
Input	Crystal	
Output	LVCMOS, LVTTL	
Number of Circuits	1	
Ratio - Input:Output	1:1	
Differential - Input:Output	No/No	
Frequency - Max	125MHz	
Divider/Multiplier	Yes/No	
Voltage - Supply	2.375 V ~ 3.465 V	
Operating Temperature	0° C ~ 70° C	
Mounting Type	Surface Mount	
Package / Case	8-TSSOP (0.173", 4.40mm Width)	
Supplier Device Package	8-TSSOP	
	Report errors?	

840021AGLF Guarantees



Quality Guarantees

We provide 90 days warranty. * If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.

SERVICE BUARANTEE

Service Guarantees

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

840021AGLF Payment Methods



840021AGLF Shipping Methods



If you have any question about 840021AGLF, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com