



#### 843002AGLFT Information

Part Number 843002AGLFT

Manufacturer IDT, Integrated Device Technology Inc

Category Integrated Circuits (ICs)

Clock/Timing - Clock Generators, PLLs,

Frequency Synthesizers

**Description** IC SYNTHESIZER LVPECL 20-TSSOP

**Package** 20-TSSOP (0.173", 4.40mm Width)

For the pricing/inventory/lead time, please contact

us

Website: https://www.heisener.com E-mail: salesdept@heisener.com



Request a Quote

## **Certified Quality**

For Reference Only

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









## 843002AGLFT Specifications

Manufacturer Part Number	843002AGLFT	
Manufacturer	IDT, Integrated Device Technology Inc	
Category	Integrated Circuits (ICs)	
	Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers	
Package	20-TSSOP (0.173", 4.40mm Width)	
Series	FemtoClock?	
Type	Frequency Synthesizer	
PLL	Yes with Bypass	
Input	Crystal	
Output	LVPECL	
Number of Circuits	1	
Ratio - Input:Output	1:2	
Differential - Input:Output	No/Yes	
Frequency - Max	226.67MHz	
Divider/Multiplier	Yes/No	
Voltage - Supply	2.97 V ~ 3.63 V	
Operating Temperature	-30°C ~ 85°C	
Mounting Type	Surface Mount	
Package / Case	20-TSSOP (0.173", 4.40mm Width)	
Supplier Device Package	20-TSSOP	
	Report	errors?

#### 843002AGLFT Guarantees



#### **Quality Guarantees**

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



#### **Service Guarantees**

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

# 843002AGLFT Payment Methods



















### 843002AGLFT Shipping Methods













If you have any question about 843002AGLFT, please do not hesitate to contact us!

Website: https://www.heisener.com E-mail: salesdept@heisener.com