



843003BGI-09LF Information

Heisener.com

For Reference Only

Part Number 843003BGI-09LF

Manufacturer IDT, Integrated Device Technology Inc

Category Integrated Circuits (ICs)

Clock/Timing - Clock Generators, PLLs,

Frequency Synthesizers

Description IC TIMING

Package 24-TSSOP (0.173", 4.40mm Width)

For the pricing/inventory/lead time, please contact

us

Website: https://www.heisener.com E-mail: salesdept@heisener.com



Request a Quote

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









843003BGI-09LF Specifications

Manufactuman Dont Number	9.42002PCI 001 E
Manufacturer Part Number	843003BGI-09LF
Manufacturer	IDT, Integrated Device Technology Inc
Category	Integrated Circuits (ICs)
	Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers
Package	24-TSSOP (0.173", 4.40mm Width)
Series	FemtoClock®
Type	Frequency Synthesizer
PLL	Yes with Bypass
Input	LVCMOS, LVTTL, Crystal
Output	LVPECL
Number of Circuits	1
Ratio - Input:Output	2:3
Differential - Input:Output	No/Yes
Frequency - Max	170MHz
Divider/Multiplier	Yes/No
Voltage - Supply	3.135V ~ 3.465V
Operating Temperature	-40°C ~ 85°C (TA)
Mounting Type	Surface Mount
Package / Case	24-TSSOP (0.173", 4.40mm Width)
Supplier Device Package	24-TSSOP
	Report errors?

843003BGI-09LF Guarantees



Quality Guarantees

We provide 90 days warranty. *

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

843003BGI-09LF Payment Methods





















843003BGI-09LF Shipping Methods













If you have any question about 843003BGI-09LF, please do not hesitate to contact us!

Website: https://www.heisener.com E-mail: salesdept@heisener.com