

844002AG-01LF

844002AG-01LF Information

	Part Number	844002AG-01LF	
STITISTY	Manufacturer	IDT, Integrated Device Technology Inc	CE11444-3 (CE1
	Category	Integrated Circuits (ICs) Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers	
	Description	IC SYNTHESIZER 2LVDS 20-TSSOP	- 1 80 - 28
	Package	20-TSSOP (0.173", 4.40mm Width)	同家海船
For Reference Only		For the pricing/inventory/lead time, please contact us Website: https://www.heisener.com E-mail: salesdept@heisener.com	Request a Quote

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



844002AG-01LF Specifications

Manufacturer Part Number	844002AG-01LF		
Manufacturer	IDT, Integrated Device Technology Inc		
Category	Integrated Circuits (ICs)		
	Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers		
Package	20-TSSOP (0.173", 4.40mm Width)		
Series	FemtoClock?		
Туре	Frequency Synthesizer		
PLL	Yes with Bypass		
Input	Clock, Crystal		
Output	LVDS		
Number of Circuits	1		
Ratio - Input:Output	2:2		
Differential - Input:Output	No/Yes		
Frequency - Max	170MHz		
Divider/Multiplier	Yes/No		
Voltage - Supply	2.375 V ~ 3.465 V		
Operating Temperature	0° C ~ 70° C		
Mounting Type	Surface Mount		
Package / Case	20-TSSOP (0.173", 4.40mm Width)		
Supplier Device Package	20-TSSOP		
	Report error	rs?	

844002AG-01LF Guarantees



Quality Guarantees

We provide 90 days warranty. * If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

844002AG-01LF Payment Methods



844002AG-01LF Shipping Methods



If you have any question about 844002AG-01LF, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com