



## **844011AGLF Information**

Heisener.com

For Reference Only

Part Number 844011AGLF

Manufacturer IDT, Integrated Device Technology Inc

**Category** Integrated Circuits (ICs)

Clock/Timing - Clock Generators, PLLs,

Frequency Synthesizers

**Description** IC TIMING

**Package** 8-TSSOP (0.173", 4.40mm Width)

For the pricing/inventory/lead time, please contact

us

Website: https://www.heisener.com E-mail: salesdept@heisener.com



Request a Quote

### **Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









## **844011AGLF Specifications**

Manufacturer Part Number	844011AGLF
Manufacturer	IDT, Integrated Device Technology Inc
Category	Integrated Circuits (ICs)
	Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers
Package	8-TSSOP (0.173", 4.40mm Width)
Series	FemtoClock®
Туре	Clock Generator
PLL	Yes
Input	Crystal
Output	LVDS
Number of Circuits	1
Ratio - Input:Output	1:1
Differential - Input:Output	No/Yes
Frequency - Max	106.25MHz
Divider/Multiplier	Yes/No
Voltage - Supply	2.375V ~ 3.465V
Operating Temperature	$0^{\circ}\text{C} \sim 70^{\circ}\text{C} \text{ (TA)}$
Mounting Type	Surface Mount
Package / Case	8-TSSOP (0.173", 4.40mm Width)
Supplier Device Package	8-TSSOP
	Report errors

#### 844011AGLF Guarantees



#### **Quality Guarantees**

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



#### **Service Guarantees**

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

# 844011AGLF Payment Methods





















### 844011AGLF Shipping Methods













If you have any question about 844011AGLF, please do not hesitate to contact us!

Website: https://www.heisener.com E-mail: salesdept@heisener.com