

**87604AGILF Information**


For Reference Only

**Part Number** [87604AGILF](#)  
**Manufacturer** IDT, Integrated Device Technology Inc  
**Category** Integrated Circuits (ICs)  
[Clock/Timing - Application Specific](#)  
**Description** IC CLK MULT/ZD BUFFER 28-TSSOP  
**Package** 28-TSSOP (0.240", 6.10mm Width)  
 For the pricing/inventory/lead time, please contact us  
 Website: <https://www.heisener.com>  
 E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)


[Request a Quote](#)
**Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


**87604AGILF Specifications**

Manufacturer Part Number	<a href="#">87604AGILF</a>
Manufacturer	IDT, Integrated Device Technology Inc
Category	Integrated Circuits (ICs) <a href="#">Clock/Timing - Application Specific</a>
Package	28-TSSOP (0.240", 6.10mm Width)
Series	-
PLL	Yes
Main Purpose	PCI Express (PCIe)
Input	LVC MOS, LV TTL, Crystal
Output	LVC MOS, LV TTL
Number of Circuits	1
Ratio - Input:Output	2:5
Differential - Input:Output	No/No
Frequency - Max	167.67MHz
Voltage - Supply	3.135 V ~ 3.465 V
Operating Temperature	-40°C ~ 85°C
Mounting Type	Surface Mount
Package / Case	28-TSSOP (0.240", 6.10mm Width)
Supplier Device Package	28-TSSOP

[Report errors?](#)

## 87604AGILF Guarantees



### Quality Guarantees

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



### Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

## 87604AGILF Payment Methods



## 87604AGILF Shipping Methods



If you have any question about 87604AGILF, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)