

8T49N008A-030NLGI8 Information

Heisener.com Gistal larver statistics	Part Number	8T49N008A-030NLGI8	
	Manufacturer	· IDT, Integrated Device Technology Inc	
	Category	Integrated Circuits (ICs) Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers	国大学校 7日 25月2日 26月2日 26月22日 2月21日
	Description	IC CLK GEN LVDS/LVPECL 40VFQFN	- 33 (BE)
	Package	40-VFQFN Exposed Pad	- 回路清井,
For Reference Only		For the pricing/inventory/lead time, please contact us	
		Website: https://www.heisener.com E-mail: salesdept@heisener.com	Request a Quote

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



8T49N008A-030NLGI8 Specifications

Manufacturer Part Number	8T49N008A-030NLGI8		
Manufacturer	IDT, Integrated Device Technology Inc		
Category	Integrated Circuits (ICs)		
	Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers		
Package	40-VFQFN Exposed Pad		
Series	FemtoClock? NG		
Туре	-		
PLL	Yes with Bypass		
Input	-		
Output	-		
Number of Circuits	1		
Ratio - Input:Output	1:8		
Differential - Input:Output	-		
Frequency - Max	-		
Divider/Multiplier	Yes/No		
Voltage - Supply	2.375 V ~ 3.465 V		
Operating Temperature	$-40^{\circ}\text{C} \sim 85^{\circ}\text{C}$		
Mounting Type	Surface Mount		
Package / Case	40-VFQFN Exposed Pad		
Supplier Device Package	40-VQFN (6x6)		
	Report errors?		

8T49N008A-030NLGI8 Guarantees



Quality Guarantees

We provide 90 days warranty. * If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

8T49N008A-030NLGI8 Payment Methods





If you have any question about 8T49N008A-030NLGI8, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com