

**932S401EFLFT Information**


For Reference Only

**Part Number** [932S401EFLFT](#)  
**Manufacturer** IDT, Integrated Device Technology Inc  
**Category** Integrated Circuits (ICs)  
[Clock/Timing - Application Specific](#)  
**Description** IC TIMING CTRL HUB PROGR 56-SSOP  
**Package** 56-BSSOP (0.295", 7.50mm Width)  
 For the pricing/inventory/lead time, please contact us  
 Website: <https://www.heisener.com>  
 E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)


[Request a Quote](#)
**Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


**932S401EFLFT Specifications**

Manufacturer Part Number	<a href="#">932S401EFLFT</a>
Manufacturer	IDT, Integrated Device Technology Inc
Category	Integrated Circuits (ICs) <a href="#">Clock/Timing - Application Specific</a>
Package	56-BSSOP (0.295", 7.50mm Width)
Series	TCH?
PLL	Yes
Main Purpose	Intel CPU, Timing Control Hub?
Input	Crystal
Output	Clock
Number of Circuits	1
Ratio - Input:Output	1:19
Differential - Input:Output	No/Yes
Frequency - Max	400MHz
Voltage - Supply	3.135 V ~ 3.465 V
Operating Temperature	0°C ~ 70°C
Mounting Type	Surface Mount
Package / Case	56-BSSOP (0.295", 7.50mm Width)
Supplier Device Package	56-SSOP

[Report errors?](#)

## 932S401EFLFT Guarantees



### Quality Guarantees

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



### Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

## 932S401EFLFT Payment Methods



## 932S401EFLFT Shipping Methods



If you have any question about 932S401EFLFT, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)