

954101DFLFT Information


For Reference Only

Part Number [954101DFLFT](#)
Manufacturer IDT, Integrated Device Technology Inc
Category Integrated Circuits (ICs)
[Clock/Timing - Application Specific](#)
Description IC CTRL HUB PROGR TIMING 56-SSOP
Package 56-TFSOP (0.240", 6.10mm Width)
 For the pricing/inventory/lead time, please contact us
 Website: <https://www.heisener.com>
 E-mail: salesdept@heisener.com


[Request a Quote](#)
Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


954101DFLFT Specifications

| | |
|-----------------------------|--|
| Manufacturer Part Number | 954101DFLFT |
| Manufacturer | IDT, Integrated Device Technology Inc |
| Category | Integrated Circuits (ICs) Clock/Timing - Application Specific |
| Package | 56-TFSOP (0.240", 6.10mm Width) |
| Series | TCH? |
| PLL | Yes |
| Main Purpose | Intel CPU, PCI Express (PCIe), Timing Control Hub? |
| Input | Crystal |
| Output | Clock |
| Number of Circuits | 1 |
| Ratio - Input:Output | 1:21 |
| Differential - Input:Output | No/Yes |
| Frequency - Max | 400MHz |
| Voltage - Supply | 3.135 V ~ 3.465 V |
| Operating Temperature | 0°C ~ 70°C |
| Mounting Type | Surface Mount |
| Package / Case | 56-TFSOP (0.240", 6.10mm Width) |
| Supplier Device Package | 56-TSSOP |

[Report errors?](#)

954101DFLFT Guarantees



Quality Guarantees

We provide 90 days warranty. *

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

954101DFLFT Payment Methods



954101DFLFT Shipping Methods



If you have any question about 954101DFLFT, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: salesdept@heisener.com