

# **9FGV1002BQ505LTGI**

### 9FGV1002BQ505LTGI Information

Part Number 9FGV1002BQ505LTGI

Manufacturer IDT, Integrated Device Technology Inc

Category Integrated Circuits (ICs)

Clock/Timing - Clock Generators, PLLs,

Frequency Synthesizers

**Description** 9FGV1002 W/INT 50M XTAL

Package 16-TFLGA Exposed Pad

For the pricing/inventory/lead time, please contact

us

Website: https://www.heisener.com E-mail: salesdept@heisener.com



Request a Quote

#### **Certified Quality**

For Reference Only

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









# 9FGV1002BQ505LTGI Specifications

Manufacturer Part Number	9FGV1002BQ505LTGI	
Manufacturer	IDT, Integrated Device Technology Inc	
Category	Integrated Circuits (ICs)	
	Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers	
Package	16-TFLGA Exposed Pad	
Series	PhiClock <sup>TM</sup>	
Type	Clock Generator	
PLL	Yes	
Input	Crystal	
Output	HCSL, LVCMOS, LVDS	
Number of Circuits	1	
Ratio - Input:Output	1:4	
Differential - Input:Output	No/Yes	
Frequency - Max	-	
Divider/Multiplier	Yes/No	
Voltage - Supply	-	
Operating Temperature	-	
Mounting Type	Surface Mount	
Package / Case	16-TFLGA Exposed Pad	
Supplier Device Package	16-LGA (3x3)	
	Report errors	?

### 9FGV1002BQ505LTGI Guarantees



#### **Quality Guarantees**

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



#### **Service Guarantees**

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

#### 9FGV1002BQ505LTGI Payment Methods



















## 9FGV1002BQ505LTGI Shipping Methods













If you have any question about 9FGV1002BQ505LTGI, please do not hesitate to contact us!

Website: https://www.heisener.com E-mail: salesdept@heisener.com