

**AD5324BRMZ Information**


For Reference Only

**Part Number** [AD5324BRMZ](#)  
**Manufacturer** Analog Devices Inc.  
**Category** Integrated Circuits (ICs)  
[Data Acquisition - Digital to Analog Converters \(DAC\)](#)  
**Description** IC DAC 12BIT QUAD VOUT 10-MSOP  
**Package** 10-TFSOP, 10-MSOP (0.118", 3.00mm Width)  
 For the pricing/inventory/lead time, please contact us  
 Website: <https://www.heisener.com>  
 E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)


[Request a Quote](#)
**Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


**AD5324BRMZ Specifications**

Manufacturer Part Number	<a href="#">AD5324BRMZ</a>
Manufacturer	Analog Devices Inc.
Category	Integrated Circuits (ICs) <a href="#">Data Acquisition - Digital to Analog Converters (DAC)</a>
Package	10-TFSOP, 10-MSOP (0.118", 3.00mm Width)
Series	-
Number of Bits	12
Number of D/A Converters	4
Settling Time	10µs
Output Type	Voltage - Buffered
Differential Output	No
Data Interface	SPI, DSP
Reference Type	External
Voltage - Supply, Analog	2.5 V ~ 5.5 V
Voltage - Supply, Digital	2.5 V ~ 5.5 V
INL/DNL (LSB)	±2, ±0.2
Architecture	String DAC
Operating Temperature	-40°C ~ 105°C
Package / Case	10-TFSOP, 10-MSOP (0.118", 3.00mm Width)
Supplier Device Package	10-MSOP
Mounting Type	-

[Report errors?](#)

## AD5324BRMZ Guarantees



### Quality Guarantees

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



### Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

## AD5324BRMZ Payment Methods



## AD5324BRMZ Shipping Methods



If you have any question about AD5324BRMZ, please do not hesitate to contact us!

Website: <https://www.heisener.com>

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