

AD5342BRUZ-REEL7

AD5342BRUZ-REEL7 Information

Mai		AD5342BRUZ-REEL7 Analog Devices Inc. Integrated Circuits (ICs) Data Acquisition - Digital to Analog Converters (DAC)	
Dese	cription	IC DAC 12BIT DUAL VOUT 28TSSOP	
Pac	kage	28-TSSOP (0.173", 4.40mm Width)	国際設計
		For the pricing/inventory/lead time, please contact us	
For Reference Only		Website: https://www.heisener.com E-mail: salesdept@heisener.com	Request a Quote

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



AD5342BRUZ-REEL7 Specifications

Manufacturer Part Number	AD5342BRUZ-REEL7		
Manufacturer	Analog Devices Inc.		
Category	Integrated Circuits (ICs)		
	Data Acquisition - Digital to Analog Converters (DAC)		
Package	28-TSSOP (0.173", 4.40mm Width)		
Series	-		
Number of Bits	12		
Number of D/A Converters	2		
Settling Time	10µs		
Output Type	Voltage - Buffered		
Differential Output	No		
Data Interface	Parallel		
Reference Type	External		
Voltage - Supply, Analog	2.5 V ~ 5.5 V		
Voltage - Supply, Digital	2.5 V ~ 5.5 V		
INL/DNL (LSB)	$\pm 2, \pm 0.2$		
Architecture	String DAC		
Operating Temperature	$-40^{\circ}\mathrm{C} \sim 105^{\circ}\mathrm{C}$		
Package / Case	28-TSSOP (0.173", 4.40mm Width)		
Supplier Device Package	28-TSSOP		
Mounting Type	-		
	Report errors?		

AD5342BRUZ-REEL7 Guarantees



Quality Guarantees

We provide 90 days warranty. * If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

DISCOVER

AD5342BRUZ-REEL7 Payment Methods



AD5342BRUZ-REEL7 Shipping Methods



If you have any question about AD5342BRUZ-REEL7, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com