



#### **AD5684BRUZ Information**



For Reference Only

Part Number AD5684BRUZ

Manufacturer Analog Devices Inc.

Category Integrated Circuits (ICs)

Data Acquisition - Digital to Analog Converters

(DAC)

**Description** IC DAC 12BIT SPI/SRL 16TSSOP **Package** 16-TSSOP (0.173", 4.40mm Width)

For the pricing/inventory/lead time, please contact

us

Website: https://www.heisener.com E-mail: salesdept@heisener.com



Request a Quote

### **Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









## **AD5684BRUZ Specifications**

Manufacturer Part Number	AD5684BRUZ
Manufacturer	Analog Devices Inc.
Category	Integrated Circuits (ICs)
	Data Acquisition - Digital to Analog Converters (DAC)
Package	16-TSSOP (0.173", 4.40mm Width)
Series	nanoDAC+?
Number of Bits	12
Number of D/A Converters	4
Settling Time	$7\mu s$
Output Type	Voltage - Buffered
Differential Output	No
Data Interface	SPI
Reference Type	External
Voltage - Supply, Analog	2.7 V ~ 5.5 V
Voltage - Supply, Digital	1.8 V ~ 5.5 V
INL/DNL (LSB)	$\pm 0.12, \pm 1 \text{ (Max)}$
Architecture	String DAC
Operating Temperature	-40°C ~ 105°C
Package / Case	16-TSSOP (0.173", 4.40mm Width)
Supplier Device Package	16-TSSOP
Mounting Type	-
	Report errors?

#### **AD5684BRUZ Guarantees**



#### **Quality Guarantees**

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



#### **Service Guarantees**

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

# **AD5684BRUZ Payment Methods**



















### **AD5684BRUZ Shipping Methods**













If you have any question about AD5684BRUZ, please do not hesitate to contact us!

Website: https://www.heisener.com E-mail: salesdept@heisener.com