

AD7175-2BRUZ

AD7175-2BRUZ Information

www.www.ener.com attattatt		AD7175-2BRUZ Analog Devices Inc. Integrated Circuits (ICs) Data Acquisition - Analog to Digital Converters (ADC)	
	Description	IC ADC 24BIT SPI/SRL 24-TSSOP	Inter Trace
	Package	24-TSSOP (0.173", 4.40mm Width)	
For Reference Only		For the pricing/inventory/lead time, please contact us Website: https://www.heisener.com E-mail: salesdept@heisener.com	Request a Quote

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



AD7175-2BRUZ Specifications

Manufacturer Part Number	AD7175-2BRUZ
Manufacturer	Analog Devices Inc.
Category	Integrated Circuits (ICs)
	Data Acquisition - Analog to Digital Converters (ADC)
Package	24-TSSOP (0.173", 4.40mm Width)
Series	-
Number of Bits	24
Sampling Rate (Per Second)	250k
Number of Inputs	2,4
Input Type	Differential, Pseudo-Differential, Single Ended
Data Interface	SPI, DSP
Configuration	MUX-ADC
Ratio - S/H:ADC	-
Number of A/D Converters	1
Architecture	Sigma-Delta
Reference Type	External, Internal
Voltage - Supply, Analog	2 V ~ 5.5 V
Voltage - Supply, Digital	2 V ~ 5.5 V
Features	Temperature Sensor
Operating Temperature	$-40^{\circ}C \sim 105^{\circ}C$
Package / Case	24-TSSOP (0.173", 4.40mm Width)
Supplier Device Package	24-TSSOP
Mounting Type	-
	Report errors?

AD7175-2BRUZ Guarantees



Quality Guarantees

We provide 90 days warranty. * If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

AD7175-2BRUZ Payment Methods



AD7175-2BRUZ Shipping Methods



If you have any question about AD7175-2BRUZ, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com