

AD7785BRUZ-REEL

AD7785BRUZ-REEL Information

Turnin		AD7785BRUZ-REEL Analog Devices Inc. Integrated Circuits (ICs) Data Acquisition - Analog to Digital Converters (ADC)	
120	Description	IC ADC 20BIT 3CH LN LP 16-TSSOP	
•	Package	16-TSSOP (0.173", 4.40mm Width)	
		For the pricing/inventory/lead time, please contact	
For Reference Only		Website: https://www.heisener.com E-mail: salesdept@heisener.com	Request a Quote

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



AD7785BRUZ-REEL Specifications

Manufacturer Part NumberAD7785BRUZ-REELManufacturerAnalog Devices Inc.CategoryIntegrated Circuits (ICs)	
Category Integrated Circuits (ICs)	
Data Acquisition - Analog to Digital Converters (ADC)	
Package 16-TSSOP (0.173", 4.40mm Width)	
Series -	
Number of Bits 20	
Sampling Rate (Per Second) 470	
Number of Inputs 3	
Input Type Differential	
Data Interface SPI, DSP	
Configuration MUX-ADC	
Ratio - S/H:ADC -	
Number of A/D Converters 1	
Architecture Sigma-Delta	
Reference Type External, Internal	
Voltage - Supply, Analog2.7 V ~ 5.25 V	
Voltage - Supply, Digital2.7 V ~ 5.25 V	
Features -	
Operating Temperature $-40^{\circ}\text{C} \sim 105^{\circ}\text{C}$	
Package / Case 16-TSSOP (0.173", 4.40mm Width)	
Supplier Device Package 16-TSSOP	
Mounting Type -	
Rep	port errors?

AD7785BRUZ-REEL Guarantees



Quality Guarantees

We provide 90 days warranty. * If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.

SERVICE BUARANTEE

Service Guarantees

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

AD7785BRUZ-REEL Payment Methods





If you have any question about AD7785BRUZ-REEL, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com