



#### **AD9520-4BCPZ Information**



For Reference Only

Part Number AD9520-4BCPZ

Manufacturer Analog Devices Inc.

Category Integrated Circuits (ICs)

Clock/Timing - Clock Generators, PLLs,

Frequency Synthesizers

**Description** IC CLOCK GEN 1.6GHZ VCO 64LFCSP

Package 64-VFQFN Exposed Pad, CSP

For the pricing/inventory/lead time, please contact

us

Website: https://www.heisener.com E-mail: salesdept@heisener.com



Request a Quote

# **Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









# **AD9520-4BCPZ Specifications**

Manufacturer Part Number	AD9520-4BCPZ	
Manufacturer	Analog Devices Inc.	
Category	Integrated Circuits (ICs)	
	Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers	
Package	64-VFQFN Exposed Pad, CSP	
Series	-	
Type	Clock Generator, Fanout Distribution	
PLL	Yes	
Input	CMOS, LVDS, LVPECL	
Output	CMOS, LVPECL	
Number of Circuits	1	
Ratio - Input:Output	2:12, 2:24	
Differential - Input:Output	Yes/Yes	
Frequency - Max	1.8GHz	
Divider/Multiplier	Yes/No	
Voltage - Supply	3.135 V ~ 3.465 V	
Operating Temperature	-40°C ~ 85°C	
Mounting Type	Surface Mount	
Package / Case	64-VFQFN Exposed Pad, CSP	
Supplier Device Package	64-LFCSP-VQ (9x9)	
	Report	errors?

#### **AD9520-4BCPZ Guarantees**



### **Quality Guarantees**

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



#### **Service Guarantees**

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

### **AD9520-4BCPZ Payment Methods**



















## **AD9520-4BCPZ Shipping Methods**













If you have any question about AD9520-4BCPZ, please do not hesitate to contact us!

Website: https://www.heisener.com E-mail: salesdept@heisener.com