



#### **ADF4002BRUZ Information**



For Reference Only

Part Number ADF4002BRUZ

Manufacturer Analog Devices Inc.

Category Integrated Circuits (ICs)

Clock/Timing - Clock Generators, PLLs,

Frequency Synthesizers

**Description** IC PLL FREQUENCY SYNTH 16-TSSOP

**Package** 16-TSSOP (0.173", 4.40mm Width)

For the pricing/inventory/lead time, please contact

us

Website: https://www.heisener.com E-mail: salesdept@heisener.com



Request a Quote

# **Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









# **ADF4002BRUZ Specifications**

Manufacturer Part Number	ADF4002BRUZ	
Manufacturer	Analog Devices Inc.	
Category	Integrated Circuits (ICs)	
	Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers	
Package	16-TSSOP (0.173", 4.40mm Width)	
Series	-	
Type	Clock/Frequency Synthesizer (RF), Phase Detector	
PLL	Yes with Bypass	
Input	CMOS, TTL	
Output	Clock	
Number of Circuits	1	
Ratio - Input:Output	2:1	
Differential - Input:Output	Yes/No	
Frequency - Max	400MHz	
Divider/Multiplier	Yes/No	
Voltage - Supply	2.7 V ~ 3.3 V	
Operating Temperature	-40°C ~ 85°C	
Mounting Type	Surface Mount	
Package / Case	16-TSSOP (0.173", 4.40mm Width)	
Supplier Device Package	16-TSSOP	
	Report	errors?

#### **ADF4002BRUZ Guarantees**



### **Quality Guarantees**

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



#### **Service Guarantees**

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

### **ADF4002BRUZ Payment Methods**





















# **ADF4002BRUZ Shipping Methods**













If you have any question about ADF4002BRUZ, please do not hesitate to contact us!

Website: https://www.heisener.com E-mail: salesdept@heisener.com