

ADG509FBRUZ

ADG509FBRUZ Information

with the second		ADG509FBRUZ Analog Devices Inc. Integrated Circuits (ICs) Interface - Analog Switches, Multiplexers, Demultiplexers	
	Description	IC MULTIPLEXER DUAL 4X1 16TSSOP	- 5 F - 5 4 4
	Package	16-TSSOP (0.173", 4.40mm Width)	- 同時時間
For Reference Only		For the pricing/inventory/lead time, please contact	E1674724
		Website: https://www.heisener.com E-mail: salesdept@heisener.com	Request a Quote

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



ADG509FBRUZ Specifications

Manufacturer Part Number	ADG509FBRUZ	
Manufacturer	Analog Devices Inc.	
Category	Integrated Circuits (ICs)	
	Interface - Analog Switches, Multiplexers, Demultiplexers	
Package	16-TSSOP (0.173", 4.40mm Width)	
Series	-	
Switch Circuit	SP4T	
Multiplexer/Demultiplexer Circuit	4:1	
Number of Circuits	2	
On-State Resistance (Max)	270 Ohm (Typ)	
Channel-to-Channel Matching (Ron)	8.1 Ohm	
Voltage - Supply, Single (V+)	15V	
Voltage - Supply, Dual (V±)	±15V	
Switch Time (Ton, Toff) (Max)	230ns, 130ns	
-3db Bandwidth	-	
Charge Injection	15pC	
Channel Capacitance (CS(off), CD(off))	3pF, 12pF	
Current - Leakage (IS(off)) (Max)	1nA	
Crosstalk	-	
Operating Temperature	-40°C ~ 85°C (TA)	
Package / Case	16-TSSOP (0.173", 4.40mm Width)	
Supplier Device Package	16-TSSOP	
	Report errors?	

ADG509FBRUZ Guarantees



Quality Guarantees

We provide 90 days warranty. * If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.

SERVICE

Service Guarantees

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

ADG509FBRUZ Payment Methods





If you have any question about ADG509FBRUZ, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com