

ADP165ACBZ-3.3-R7 Information


For Reference Only

Part Number [ADP165ACBZ-3.3-R7](#)
Manufacturer Analog Devices Inc.
Category Integrated Circuits (ICs)
 [PMIC - Voltage Regulators - Linear](#)
Description IC REG LINEAR 3.3V 150MA 4WLCSP
Package 4-UFBGA, WLCSP
 For the pricing/inventory/lead time, please contact us
 Website: <https://www.heisener.com>
 E-mail: salesdept@heisener.com


[Request a Quote](#)
Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


ADP165ACBZ-3.3-R7 Specifications

Manufacturer Part Number	ADP165ACBZ-3.3-R7
Manufacturer	Analog Devices Inc.
Category	Integrated Circuits (ICs) PMIC - Voltage Regulators - Linear
Package	4-UFBGA, WLCSP
Series	-
Output Configuration	Positive
Output Type	Fixed
Number of Regulators	1
Voltage - Input (Max)	5.5V
Voltage - Output (Min/Fixed)	3.3V
Voltage - Output (Max)	-
Voltage Dropout (Max)	0.225V @ 150mA
Current - Output	150mA
Current - Quiescent (Iq)	-
Current - Supply (Max)	2.4µA ~ 6.2µA
PSRR	60dB ~ 50dB (100Hz ~ 1kHz)
Control Features	Enable
Protection Features	Over Current, Over Temperature, Under Voltage Lockout (UVLO)
Operating Temperature	-40°C ~ 125°C
Mounting Type	Surface Mount
Package / Case	4-UFBGA, WLCSP
Supplier Device Package	4-WLCSP (0.97x0.97)

[Report errors?](#)

ADP165ACBZ-3.3-R7 Guarantees



Quality Guarantees

We provide 90 days warranty. *

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

ADP165ACBZ-3.3-R7 Payment Methods



ADP165ACBZ-3.3-R7 Shipping Methods



If you have any question about ADP165ACBZ-3.3-R7, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: salesdept@heisener.com