

ADP1740ACPZ-1.0-R7 Information


For Reference Only

Part Number [ADP1740ACPZ-1.0-R7](#)
Manufacturer Analog Devices Inc.
Category Integrated Circuits (ICs)
[PMIC - Voltage Regulators - Linear](#)
Description IC REG LINEAR 1V 2A 16LFCSP
Package 16-WQFN Exposed Pad, CSP
 For the pricing/inventory/lead time, please contact us
 Website: <https://www.heisener.com>
 E-mail: salesdept@heisener.com


[Request a Quote](#)
Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


ADP1740ACPZ-1.0-R7 Specifications

Manufacturer Part Number	ADP1740ACPZ-1.0-R7
Manufacturer	Analog Devices Inc.
Category	Integrated Circuits (ICs) PMIC - Voltage Regulators - Linear
Package	16-WQFN Exposed Pad, CSP
Series	-
Output Configuration	Positive
Output Type	Fixed
Number of Regulators	1
Voltage - Input (Max)	3.6V
Voltage - Output (Min/Fixed)	1V
Voltage - Output (Max)	-
Voltage Dropout (Max)	-
Current - Output	2A
Current - Quiescent (Iq)	-
Current - Supply (Max)	800µA ~ 1.8mA
PSRR	-
Control Features	Enable, Power Good, Soft Start
Protection Features	Over Current, Over Temperature, Reverse Polarity
Operating Temperature	-40°C ~ 125°C
Mounting Type	Surface Mount
Package / Case	16-WQFN Exposed Pad, CSP
Supplier Device Package	16-LFCSP-WQ (4x4)

[Report errors?](#)

ADP1740ACPZ-1.0-R7 Guarantees



Quality Guarantees

We provide 90 days warranty. *

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

ADP1740ACPZ-1.0-R7 Payment Methods



ADP1740ACPZ-1.0-R7 Shipping Methods



If you have any question about ADP1740ACPZ-1.0-R7, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: salesdept@heisener.com