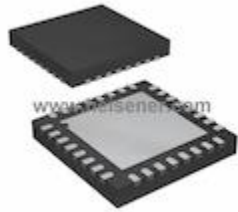


ADP1850ACPZ-R7 Information


For Reference Only

Part Number [ADP1850ACPZ-R7](#)
Manufacturer Analog Devices Inc.
Category Integrated Circuits (ICs)
[PMIC - Voltage Regulators - DC DC Switching Controllers](#)
Description IC REG CTRLR BUCK 32LFCSP
Package 32-VFQFN Exposed Pad, CSP
 For the pricing/inventory/lead time, please contact us
 Website: <https://www.heisener.com>
 E-mail: salesdept@heisener.com


[Request a Quote](#)
Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


ADP1850ACPZ-R7 Specifications

Manufacturer Part Number	ADP1850ACPZ-R7
Manufacturer	Analog Devices Inc.
Category	Integrated Circuits (ICs) PMIC - Voltage Regulators - DC DC Switching Controllers
Package	32-VFQFN Exposed Pad, CSP
Series	-
Output Type	Transistor Driver
Function	Step-Down
Output Configuration	Positive
Topology	Buck
Number of Outputs	2
Output Phases	2
Voltage - Supply (Vcc/Vdd)	2.75 V ~ 20 V
Frequency - Switching	200kHz ~ 1.5MHz
Duty Cycle (Max)	90%
Synchronous Rectifier	Yes
Clock Sync	Yes
Serial Interfaces	-
Control Features	Current Limit, Enable, Frequency Control, Power Good, Soft Start, Tracking
Operating Temperature	-40°C ~ 85°C (TA)
Package / Case	32-VFQFN Exposed Pad, CSP
Supplier Device Package	32-LFCSP-VQ (5x5)

[Report errors?](#)

ADP1850ACPZ-R7 Guarantees



Quality Guarantees

We provide 90 days warranty. *

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

ADP1850ACPZ-R7 Payment Methods



ADP1850ACPZ-R7 Shipping Methods



If you have any question about ADP1850ACPZ-R7, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: salesdept@heisener.com