

ADP3050ARZ-3.3-RL7 Information


For Reference Only

Part Number [ADP3050ARZ-3.3-RL7](#)
Manufacturer Analog Devices Inc.
Category Integrated Circuits (ICs)
[PMIC - Voltage Regulators - DC DC Switching Regulators](#)
Description IC REG MULT CONFIG 3.3V 1A 8SOIC
Package 8-SOIC (0.154", 3.90mm Width)
 For the pricing/inventory/lead time, please contact us
 Website: <https://www.heisener.com>
 E-mail: salesdept@heisener.com


[Request a Quote](#)
Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


ADP3050ARZ-3.3-RL7 Specifications

Manufacturer Part Number	ADP3050ARZ-3.3-RL7
Manufacturer	Analog Devices Inc.
Category	Integrated Circuits (ICs) PMIC - Voltage Regulators - DC DC Switching Regulators
Package	8-SOIC (0.154", 3.90mm Width)
Series	-
Function	Step-Down, Step-Up/Step-Down
Output Configuration	Positive
Topology	Buck, Buck-Boost, SEPIC
Output Type	Fixed
Number of Outputs	1
Voltage - Input (Min)	3.6V
Voltage - Input (Max)	30V
Voltage - Output (Min/Fixed)	3.3V
Voltage - Output (Max)	-
Current - Output	1A (Switch)
Frequency - Switching	200kHz
Synchronous Rectifier	No
Operating Temperature	-40°C ~ 85°C (TA)
Mounting Type	Surface Mount
Package / Case	8-SOIC (0.154", 3.90mm Width)
Supplier Device Package	8-SOIC

[Report errors?](#)

ADP3050ARZ-3.3-RL7 Guarantees



Quality Guarantees

We provide 90 days warranty. *

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

ADP3050ARZ-3.3-RL7 Payment Methods



ADP3050ARZ-3.3-RL7 Shipping Methods



If you have any question about ADP3050ARZ-3.3-RL7, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: salesdept@heisener.com