

CD74HC4053M96 Information


For Reference Only

Part Number [CD74HC4053M96](#)
Manufacturer Texas Instruments
Category Integrated Circuits (ICs)
[Interface - Analog Switches, Multiplexers, Demultiplexers](#)
Description IC MUX/DEMUX TRIPLE 2X1 16SOIC
Package 16-SOIC (0.154", 3.90mm Width)
 For the pricing/inventory/lead time, please contact us
 Website: <https://www.heisener.com>
 E-mail: salesdept@heisener.com


[Request a Quote](#)
Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


CD74HC4053M96 Specifications

Manufacturer Part Number	CD74HC4053M96
Manufacturer	Texas Instruments
Category	Integrated Circuits (ICs) Interface - Analog Switches, Multiplexers, Demultiplexers
Package	16-SOIC (0.154", 3.90mm Width)
Series	-
Switch Circuit	SPDT
Multiplexer/Demultiplexer Circuit	2:1
Number of Circuits	3
On-State Resistance (Max)	130 Ohm
Channel-to-Channel Matching (Ron)	5 Ohm
Voltage - Supply, Single (V+)	2 V ~ 6 V
Voltage - Supply, Dual (V±)	±1 V ~ 5 V
Switch Time (Ton, Toff) (Max)	-
-3db Bandwidth	200MHz
Charge Injection	-
Channel Capacitance (CS(off), CD(off))	8pF
Current - Leakage (IS(off)) (Max)	100nA
Crosstalk	-
Operating Temperature	-55°C ~ 125°C (TA)
Package / Case	16-SOIC (0.154", 3.90mm Width)
Supplier Device Package	16-SOIC

[Report errors?](#)

CD74HC4053M96 Guarantees



Quality Guarantees

We provide 90 days warranty. *

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

CD74HC4053M96 Payment Methods



CD74HC4053M96 Shipping Methods



If you have any question about CD74HC4053M96, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: salesdept@heisener.com