

**CDCV850DGG Information**


For Reference Only

**Part Number** [CDCV850DGG](#)  
**Manufacturer** Texas Instruments  
**Category** Integrated Circuits (ICs)  
[Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers](#)  
**Description** IC PLL CLOCK DRVR 2.5V 48-TSSOP  
**Package** 48-TFSOP (0.240", 6.10mm Width)  
 For the pricing/inventory/lead time, please contact us  
 Website: <https://www.heisener.com>  
 E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)


[Request a Quote](#)
**Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


**CDCV850DGG Specifications**

Manufacturer Part Number	<a href="#">CDCV850DGG</a>
Manufacturer	Texas Instruments
Category	Integrated Circuits (ICs) <a href="#">Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers</a>
Package	48-TFSOP (0.240", 6.10mm Width)
Series	-
Type	PLL Clock Driver
PLL	Yes with Bypass
Input	LVTTL, SSTL-2
Output	SSTL-2
Number of Circuits	1
Ratio - Input:Output	1:11
Differential - Input:Output	Yes/Yes
Frequency - Max	140MHz
Divider/Multiplier	No/No
Voltage - Supply	2.3 V ~ 2.7 V
Operating Temperature	0°C ~ 85°C
Mounting Type	Surface Mount
Package / Case	48-TFSOP (0.240", 6.10mm Width)
Supplier Device Package	48-TSSOP

[Report errors?](#)

## CDCV850DGG Guarantees



### Quality Guarantees

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



### Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

## CDCV850DGG Payment Methods



## CDCV850DGG Shipping Methods



If you have any question about CDCV850DGG, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)