

CS48-35P Information


For Reference Only

Part Number [CS48-35P](#)
Manufacturer Central Semiconductor Corp
Category Discrete Semiconductor Products
 [Thyristors - SCRs](#)
Description SCR PHASE CONTROL TO48
Package TO-208AA, TO-48-3, Stud
 For the pricing/inventory/lead time, please contact us
 Website: <https://www.heisener.com>
 E-mail: salesdept@heisener.com


[Request a Quote](#)
Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


CS48-35P Specifications

| | |
|--|--|
| Manufacturer Part Number | CS48-35P |
| Manufacturer | Central Semiconductor Corp |
| Category | Discrete Semiconductor Products Thyristors - SCRs |
| Package | TO-208AA, TO-48-3, Stud |
| Series | - |
| Voltage - Off State | 1000V |
| Voltage - Gate Trigger (Vgt) (Max) | 1.5V |
| Current - Gate Trigger (Igt) (Max) | 40mA |
| Voltage - On State (Vtm) (Max) | 2.3V |
| Current - On State (It (AV)) (Max) | - |
| Current - On State (It (RMS)) (Max) | 35A |
| Current - Hold (Ih) (Max) | 100mA |
| Current - Off State (Max) | 20μA |
| Current - Non Rep. Surge 50, 60Hz (Itsm) | 330A @ 100Hz |
| SCR Type | Standard Recovery |
| Operating Temperature | -40°C ~ 125°C |
| Mounting Type | Chassis, Stud Mount |
| Package / Case | TO-208AA, TO-48-3, Stud |
| Supplier Device Package | TO-48 |
| Report errors? | |

CS48-35P Guarantees



Quality Guarantees

We provide 90 days warranty. *

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

CS48-35P Payment Methods



CS48-35P Shipping Methods



If you have any question about CS48-35P, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: salesdept@heisener.com