

CS8126-1YDPSR7G Information


For Reference Only

Part Number [CS8126-1YDPSR7G](#)
Manufacturer ON Semiconductor
Category Integrated Circuits (ICs)
[PMIC - Voltage Regulators - Linear](#)
Description IC REG LINEAR 5V 750MA D2PAK-7
Package TO-263-8, D2Pak (7 Leads + Tab), TO-263CA
 For the pricing/inventory/lead time, please contact us
 Website: <https://www.heisener.com>
 E-mail: salesdept@heisener.com


[Request a Quote](#)
Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


CS8126-1YDPSR7G Specifications

Manufacturer Part Number	CS8126-1YDPSR7G
Manufacturer	ON Semiconductor
Category	Integrated Circuits (ICs) PMIC - Voltage Regulators - Linear
Package	TO-263-8, D2Pak (7 Leads + Tab), TO-263CA
Series	-
Output Configuration	Positive
Output Type	Fixed
Number of Regulators	1
Voltage - Input (Max)	26V
Voltage - Output (Min/Fixed)	5V
Voltage - Output (Max)	-
Voltage Dropout (Max)	0.6V @ 500mA
Current - Output	750mA
Current - Quiescent (Iq)	-
Current - Supply (Max)	70mA ~ 100mA
PSRR	75dB (120Hz)
Control Features	Reset
Protection Features	Over Temperature, Reverse Polarity, Short Circuit
Operating Temperature	-40°C ~ 125°C
Mounting Type	Surface Mount
Package / Case	TO-263-8, D2Pak (7 Leads + Tab), TO-263CA
Supplier Device Package	D2PAK-7
Report errors?	

CS8126-1YDPSR7G Guarantees



Quality Guarantees

We provide 90 days warranty. *

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

CS8126-1YDPSR7G Payment Methods



CS8126-1YDPSR7G Shipping Methods



If you have any question about CS8126-1YDPSR7G, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: salesdept@heisener.com