

CS8183YDWF20G

CS8183YDWF20G Information

Conner com	Part Number	CS8183YDWF20G	
	Manufacturer	ON Semiconductor	
	Category	Integrated Circuits (ICs) PMIC - Voltage Regulators - Linear	
	Description	IC REG LIN POS ADJ 20SOIC	
	Package	20-SOIC (0.295", 7.50mm Width)	
Ale.		For the pricing/inventory/lead time, please contact us	
For Reference Only		Website: https://www.heisener.com E-mail: salesdept@heisener.com	



Request a Quote

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



CS8183YDWF20G Specifications

Manufacturer Part Number	CS8183YDWF20G	
Manufacturer	ON Semiconductor	
Category	Integrated Circuits (ICs)	
	PMIC - Voltage Regulators - Linear	
Package	20-SOIC (0.295", 7.50mm Width)	
Series	-	
Output Configuration	Positive	
Output Type	Adjustable	
Number of Regulators	2	
Voltage - Input (Max)	45V	
Voltage - Output (Min/Fixed)	Tracking	
Voltage - Output (Max)	-	
Voltage Dropout (Max)	0.6V @ 200mA, 0.6V @ 200mA	
Current - Output	200mA, 200mA	
Current - Quiescent (Iq)	-	
Current - Supply (Max)	55µA ~ 25mA	
PSRR	60dB (120Hz)	
Control Features	Enable	
Protection Features	Over Temperature, Reverse Polarity, Short Circuit	
Operating Temperature	-40° C ~ 125° C	
Mounting Type	Surface Mount	
Package / Case	20-SOIC (0.295", 7.50mm Width)	
Supplier Device Package	20-SOIC	
		Report errors?

CS8183YDWF20G Guarantees



Quality Guarantees

We provide 90 days warranty. * If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

CS8183YDWF20G Payment Methods





If you have any question about CS8183YDWF20G, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com