

CY22050ZXC-150T

CY22050ZXC-150T Information

	Part Number	CY22050ZXC-150T	
Strew helsener.com	Manufacturer	Cypress Semiconductor Corp	
	Category	Integrated Circuits (ICs) Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers	
	Description	IC CLOCK GEN PROG 16-TSSOP	
	Package	16-TSSOP (0.173", 4.40mm Width)	国際派権権
For Reference Only		For the pricing/inventory/lead time, please contact us Website: https://www.heisener.com E-mail: salesdept@heisener.com	Request a Quote

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



CY22050ZXC-150T Specifications

Manufacturer Part Number	CY22050ZXC-150T		
Manufacturer	Cypress Semiconductor Corp		
Category	Integrated Circuits (ICs)		
	Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers		
Package	16-TSSOP (0.173", 4.40mm Width)		
Series	-		
Туре	Clock Generator, Fanout Distribution		
PLL	Yes with Bypass		
Input	LVCMOS, LVTTL, Crystal		
Output	CMOS		
Number of Circuits	1		
Ratio - Input:Output	1:6		
Differential - Input:Output	No/No		
Frequency - Max	150MHz		
Divider/Multiplier	Yes/No		
Voltage - Supply	2.5V, 3.3V		
Operating Temperature	0° C ~ 70° C		
Mounting Type	Surface Mount		
Package / Case	16-TSSOP (0.173", 4.40mm Width)		
Supplier Device Package	16-TSSOP		
	Report errors		

CY22050ZXC-150T Guarantees



Quality Guarantees

We provide 90 days warranty. * If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.

SERVICE BUARANTEE

Service Guarantees

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

CY22050ZXC-150T Payment Methods



CY22050ZXC-150T Shipping Methods



If you have any question about CY22050ZXC-150T, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com