

CY2309CZXC-1HT

CY2309CZXC-1HT Information

	Part Number	CY2309CZXC-1HT	
Story helsener.com	Manufacturer	Cypress Semiconductor Corp	
	Category	Integrated Circuits (ICs) Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers	
	Description	IC CLK ZDB 90UT 133MHZ 16TSSOP	
	Package	16-TSSOP (0.173", 4.40mm Width)	□
For Reference Only		For the pricing/inventory/lead time, please contact us Website: https://www.heisener.com E-mail: salesdept@heisener.com	Request a Quote

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



CY2309CZXC-1HT Specifications

Manufacturer Part Number	CY2309CZXC-1HT		
Manufacturer	Cypress Semiconductor Corp		
Category	Integrated Circuits (ICs)		
	Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers		
Package	16-TSSOP (0.173", 4.40mm Width)		
Series	-		
Туре	Fanout Buffer (Distribution), Zero Delay Buffer		
PLL	Yes with Bypass		
Input	LVCMOS, LVTTL		
Output	LVCMOS		
Number of Circuits	1		
Ratio - Input:Output	1:9		
Differential - Input:Output	No/No		
Frequency - Max	133.33MHz		
Divider/Multiplier	No/No		
Voltage - Supply	3 V ~ 3.6 V		
Operating Temperature	0° C ~ 70° C		
Mounting Type	Surface Mount		
Package / Case	16-TSSOP (0.173", 4.40mm Width)		
Supplier Device Package	16-TSSOP		
	Report errors		

CY2309CZXC-1HT Guarantees



Quality Guarantees

We provide 90 days warranty. * If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.

SERVICE EUARANTEE

Service Guarantees

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

CY2309CZXC-1HT Payment Methods



CY2309CZXC-1HT Shipping Methods



If you have any question about CY2309CZXC-1HT, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com