



# **CY25100ZXIF Information**

www.hetsener.com

For Reference Only

Part Number CY25100ZXIF

Manufacturer Cypress Semiconductor Corp Category Integrated Circuits (ICs)

Clock/Timing - Clock Generators, PLLs,

Frequency Synthesizers

**Description**IC CLOCK GEN PROG 8-TSSOP**Package**8-TSSOP (0.173", 4.40mm Width)

For the pricing/inventory/lead time, please contact

us

Website: https://www.heisener.com E-mail: salesdept@heisener.com



Request a Quote

## **Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









## **CY25100ZXIF Specifications**

Manufacturer Part Number	CY25100ZXIF
Manufacturer	Cypress Semiconductor Corp
Category	Integrated Circuits (ICs)
	Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers
Package	8-TSSOP (0.173", 4.40mm Width)
Series	-
Type	Fanout Buffer (Distribution), Spread Spectrum Clock Generator
PLL	Yes
Input	CMOS, Crystal
Output	CMOS
Number of Circuits	1
Ratio - Input:Output	1:2
Differential - Input:Output	No/No
Frequency - Max	200MHz
Divider/Multiplier	Yes/No
Voltage - Supply	3.13 V ~ 3.45 V
Operating Temperature	-40°C ~ 85°C
Mounting Type	Surface Mount
Package / Case	8-TSSOP (0.173", 4.40mm Width)
Supplier Device Package	8-TSSOP
	Report errors?

#### **CY25100ZXIF Guarantees**



#### **Quality Guarantees**

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



#### **Service Guarantees**

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

# **CY25100ZXIF Payment Methods**



















### **CY25100ZXIF Shipping Methods**













If you have any question about CY25100ZXIF, please do not hesitate to contact us!

Website: https://www.heisener.com E-mail: salesdept@heisener.com