

CY25200-ZXC010AT

CY25200-ZXC010AT Information

Structure con-		CY25200-ZXC010AT Cypress Semiconductor Corp Integrated Circuits (ICs)	
	Description Package	Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers IC PROG SSCLK GENERATOR 16-TSSOP 16-TSSOP (0.173", 4.40mm Width)	
For Reference Only		For the pricing/inventory/lead time, please contact us Website: https://www.heisener.com E-mail: salesdept@heisener.com	Request a Quote

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



CY25200-ZXC010AT Specifications

Manufacturer Part Number	CY25200-ZXC010AT		
Manufacturer	Cypress Semiconductor Corp		
Category	Integrated Circuits (ICs)		
	Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers		
Package	16-TSSOP (0.173", 4.40mm Width)		
Series	-		
Туре	Fanout Buffer (Distribution), Spread Spectrum Clock Generator		
PLL	Yes with Bypass		
Input	Clock, Crystal		
Output	VDDL		
Number of Circuits	1		
Ratio - Input:Output	1:6		
Differential - Input:Output	No/No		
Frequency - Max	200MHz		
Divider/Multiplier	Yes/No		
Voltage - Supply	2.375 V ~ 3.465 V		
Operating Temperature	0° C ~ 70° C		
Mounting Type	Surface Mount		
Package / Case	16-TSSOP (0.173", 4.40mm Width)		
Supplier Device Package	16-TSSOP		
	Report er	rors?	

CY25200-ZXC010AT Guarantees



Quality Guarantees

We provide 90 days warranty. * If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.

SERVICE EUARANTEE

Service Guarantees

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

DISCOVER

CY25200-ZXC010AT Payment Methods



CY25200-ZXC010AT Shipping Methods



If you have any question about CY25200-ZXC010AT, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com