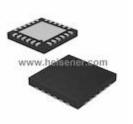


## **CY2544FI Information**



For Reference Only

Part Number CY2544FI

Manufacturer Cypress Semiconductor Corp Category Integrated Circuits (ICs)

Clock/Timing - Clock Generators, PLLs,

Frequency Synthesizers

**Description** IC CLK GEN FIELD QUAD PROG 24QFN

Package 24-VFQFN Exposed Pad

For the pricing/inventory/lead time, please contact

us

Website: https://www.heisener.com E-mail: salesdept@heisener.com



Request a Quote

## **Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









# **CY2544FI Specifications**

Manufacturer Part Number	CY2544FI
Manufacturer	Cypress Semiconductor Corp
Category	Integrated Circuits (ICs)
	Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers
Package	24-VFQFN Exposed Pad
Series	-
Type	Fanout Distribution, Fractional N Synthesizer, Multiplexer, Spread Spectrum Clock Generator
PLL	Yes with Bypass
Input	LVCMOS, Crystal
Output	Clock
Number of Circuits	1
Ratio - Input:Output	1:9
Differential - Input:Output	No/No
Frequency - Max	166MHz
Divider/Multiplier	Yes/No
Voltage - Supply	2.25 V ~ 3.6 V
Operating Temperature	-40°C ~ 85°C
Mounting Type	Surface Mount
Package / Case	24-VFQFN Exposed Pad
Supplier Device Package	24-QFN-EP (4x4)
	Report errors?

## **CY2544FI Guarantees**



### **Quality Guarantees**

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



#### **Service Guarantees**

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

## **CY2544FI Payment Methods**



















# **CY2544FI Shipping Methods**













If you have any question about CY2544FI, please do not hesitate to contact us!

Website: https://www.heisener.com E-mail: salesdept@heisener.com